

COMPAL CONFIDENTIAL

MODEL NAME : QXW10
PCB NO : LA-7902P (DA60000PV00)
BOM P/N : 4319F831L01
GPIO MAP: E4_VC_GPIO_map_rev_1.1

Korbel 15 UMA

Ivy/Sandy Bridge + Panther POINT(HM77 w/o Vpro,/QM77 w/Vpro)

www.aitech1.ru

2012-03-07

REV : 1.0 (A00)

@ : Nopop Component

CONN@ : Connector Component

	MB Type	BOM P/N	
*	TPM(R1)	4319F831L01	1@ 3@ 5@
	TPM DIS(R1)	4319F831L02	2@ 3@
	TPM(R3)	4319F831L03	1@ 3@ 5@
	TPM DIS(R3)	4319F831L04	2@ 3@
*	HM77 w/o Vpro		
	QM77 w/ Vpro		
	PCH XDP		PXDP@
	HDMI LOGO		46@

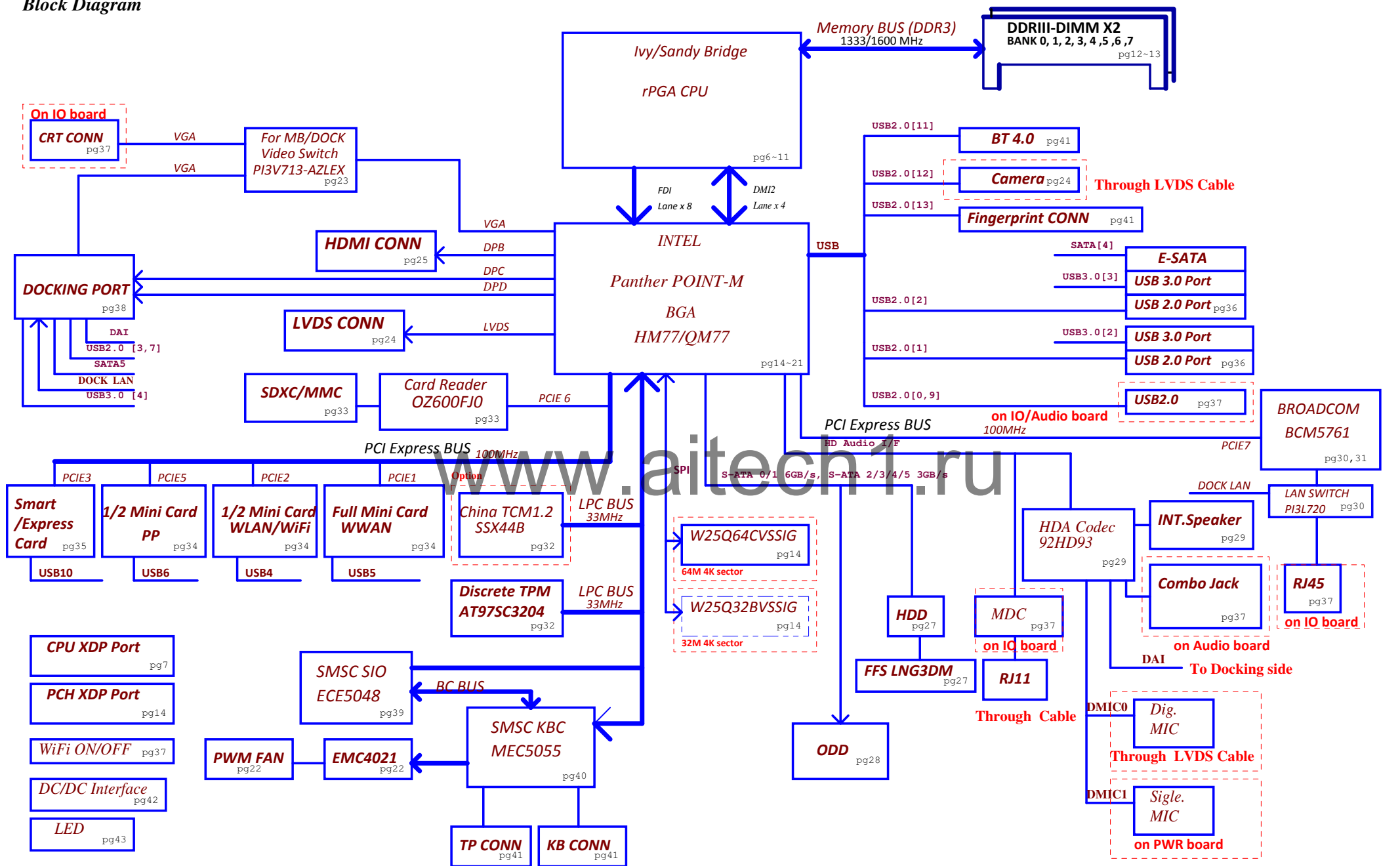
MB PCB	
Part Number	Description
DA60000PV00	PCB 0LH LA-7902P REV0 M/B UMA

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Title		Cover Sheet	
Size	Document Number	LA-7902P	Rev 1.0
Date:	Wednesday, March 07, 2012	Sheet 1 of 61	

Block Diagram



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UMA Block Diagram

LA-7902P

Rev	1.0
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Date: Wednesday, March 07, 2012

Sheet 2 of 61

POWER STATES

Signal State	SLP S3#	SLP S4#	SLP S5#	SLP A#	ALWAYS PLANE	M PLANE	SUS PLANE	RUN PLANE	CLOCKS
S0 (Full ON) / M0	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S3 (Suspend to RAM) / M3	LOW	HIGH	HIGH	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to DISK) / M3	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF	OFF
S5 (SOFT OFF) / M3	LOW	LOW	LOW	HIGH	ON	ON	OFF	OFF	OFF
S3 (Suspend to RAM) / M-OFF	LOW	HIGH	HIGH	LOW	ON	OFF	ON	OFF	OFF
S4 (Suspend to DISK) / M-OFF	LOW	LOW	HIGH	LOW	ON	OFF	OFF	OFF	OFF
S5 (SOFT OFF) / M-OFF	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF	OFF

PM TABLE

power plane State	+15V_ALW +5V_ALW +3.3V_ALW_PCH +3.3V_RTC_LDO	+3.3V_SUS +1.5V_MEM	+5V_RUN +3.3V_RUN +1.8V_RUN +1.5V_RUN +0.75V_DDR_VTT +VCC_CORE +1.05V_RUN_VTT +1.05V_RUN	+3.3V_M +1.05V_M	+3.3V_M +1.05V_M (M-OFF)
S0	ON	ON	ON	ON	ON
S3	ON	ON	OFF	ON	OFF
S5 S4/AC	ON	OFF	OFF	ON	OFF
S5 S4/AC don't exist	OFF	OFF	OFF	OFF	OFF

need to update Power Status and PM
Table

USB 3.0 PORT#	Connetion
1	NA
2	JUSB1 (Left side)
3	JESA1 (Left side)
4	MLK DOCK

USB PORT#	DESTINATION
0	JUSB (Right side-IO/B)
1	JUSB1 (Left side)
2	JESA1 (Left side ESATA)
3	DOCKING
4	WLAN
5	WWAN
*1 6	JMINI3(Flash)-for w/ Vpro
*1 7	DOCKING
8	NA
9	JUSB (Right side-Audio/B)
10	Express card
11	Bluetooth
12	Camera
13	BIO

*1: HM76 don't support port 6,7

SATA	DESTINATION
SATA 0	HDD
SATA 1	ODD/ E3 Module Bay
SATA 2	NA
SATA 3	NA
SATA 4	ESATA
SATA 5	Dock

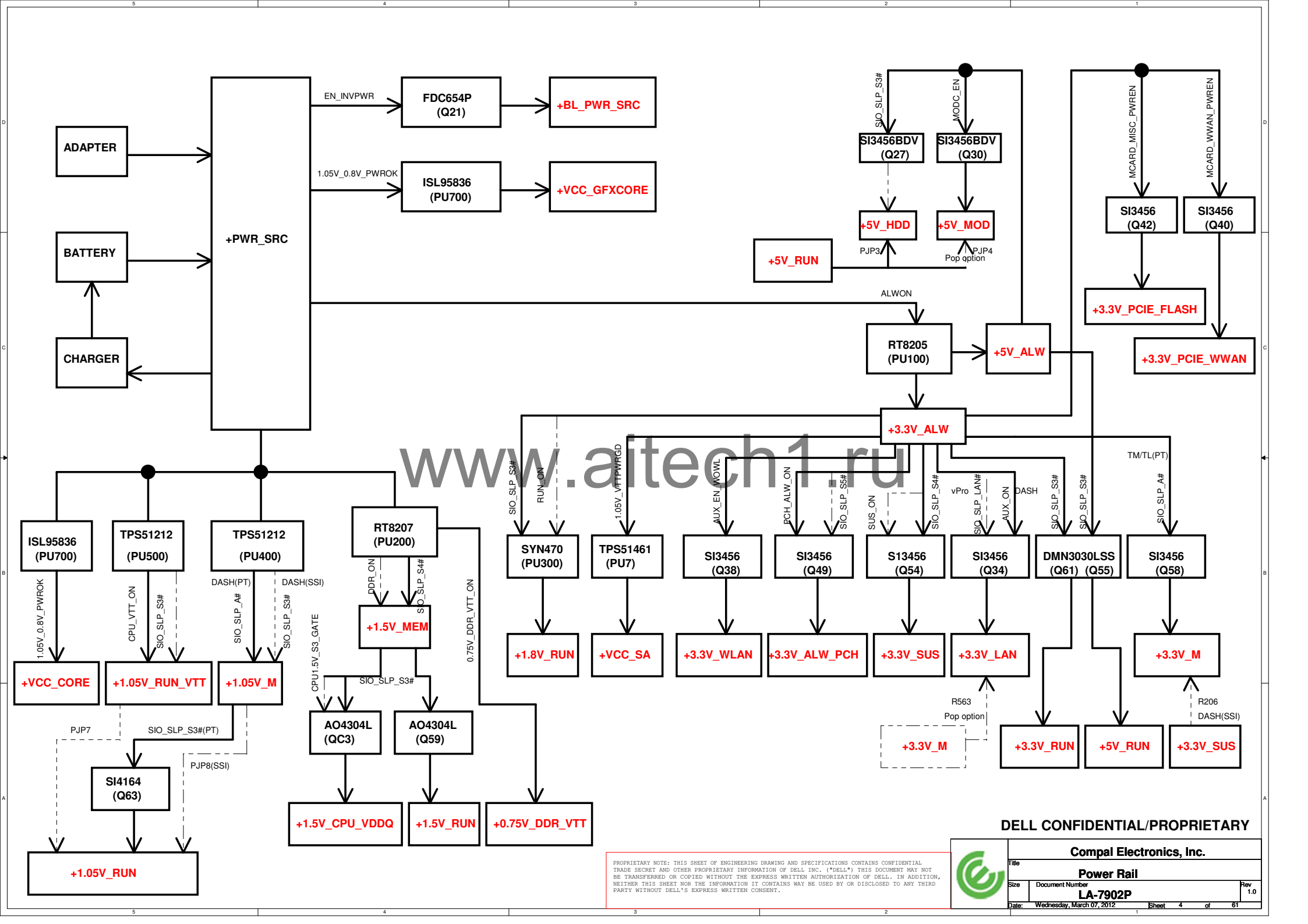
UMA DP/HDMI Port	Connetion
Port B	MB HDMI Conn
Port C	Dock DP port 2
Port D	Dock DP port 1

PCI EXPRESS	DESTINATION
Lane 1	MINI CARD-1 WWAN
Lane 2	MINI CARD-2 WLAN
Lane 3	Express card
Lane 4	None
Lane 5	1/2vMINI CARD-3 PCIE
Lane 6	MMI
Lane 7	10/100/1G LOM
Lane 8	None

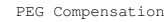
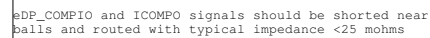
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Index and Config.			
Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	3 of 61

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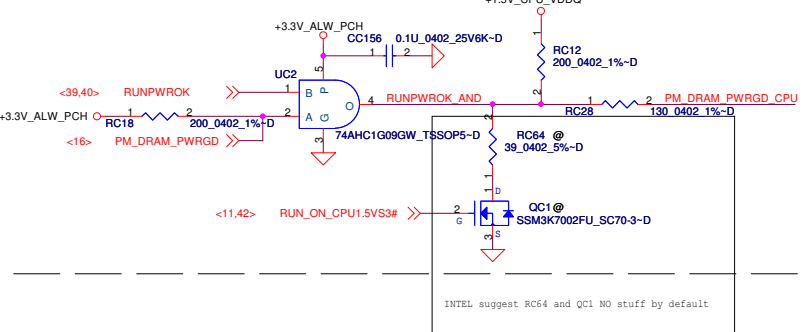


PCI EXPRESS* - GRAPHICS

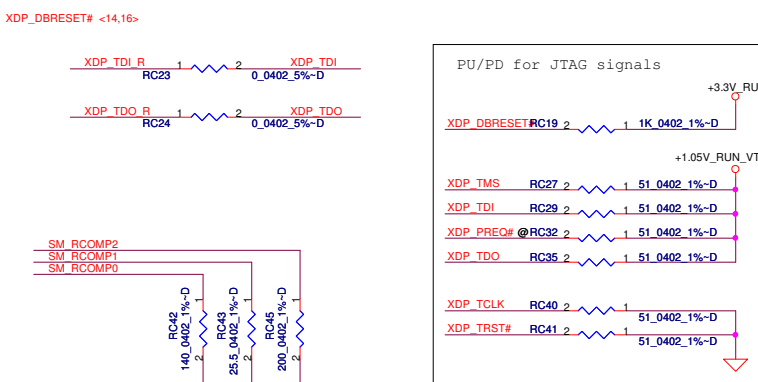
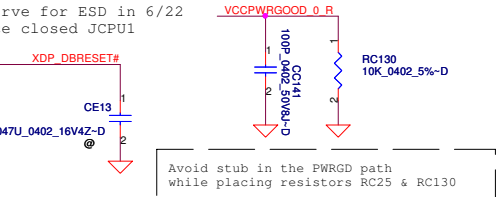
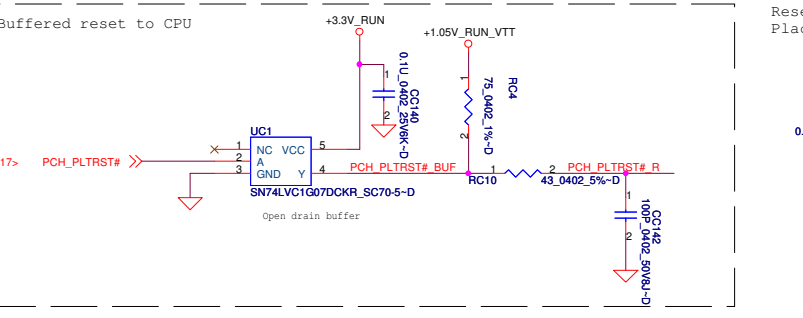
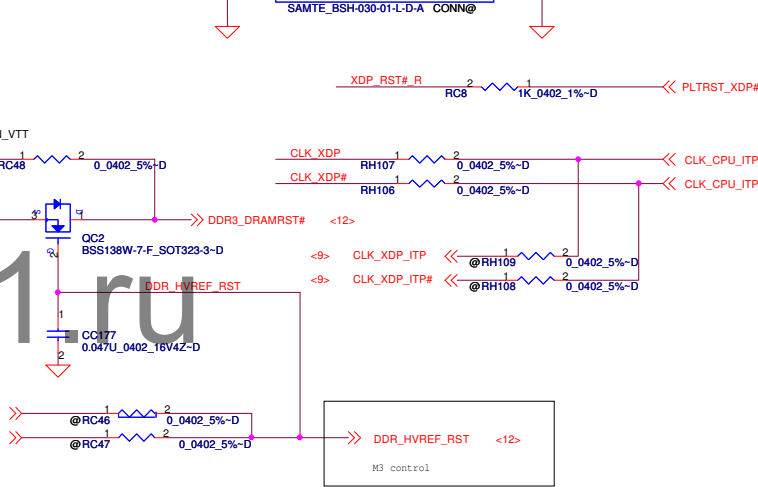
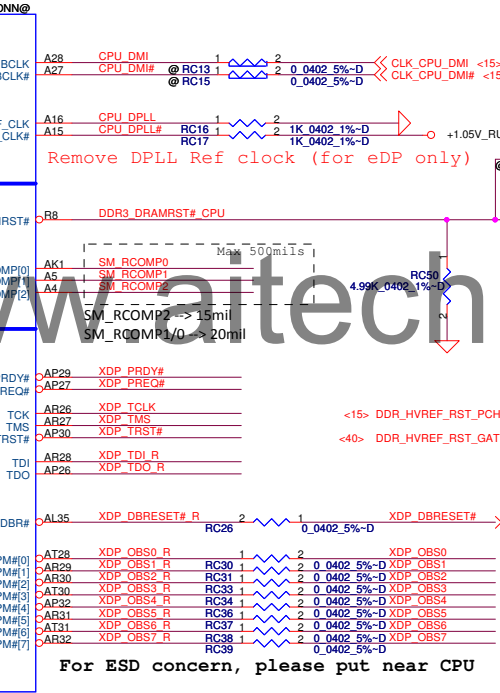
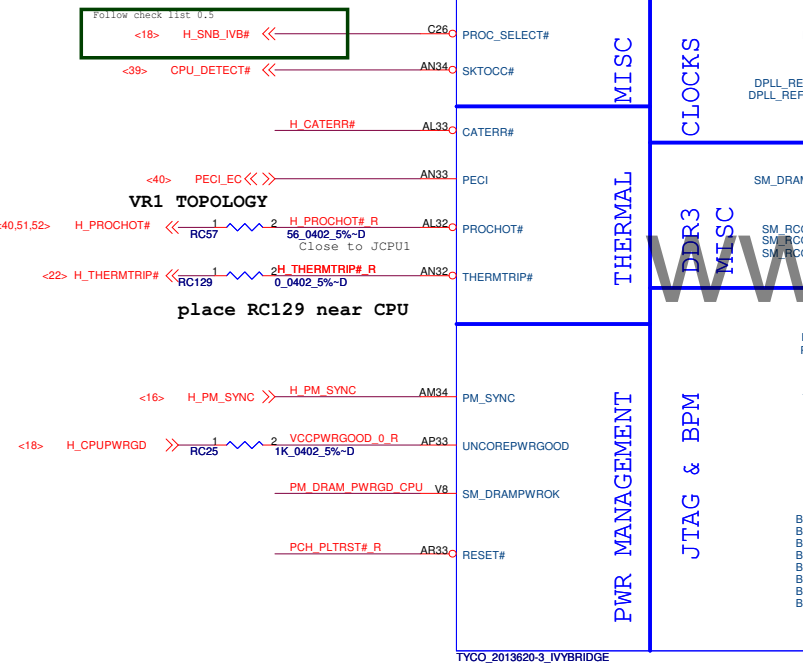
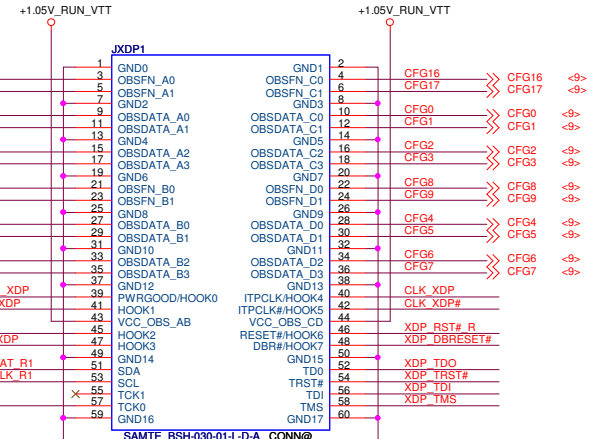
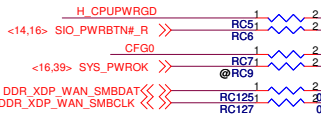


Date: Wednesday, March 07, 2012 Sheet 6 of 61

Follow DG Rev0.71 SM_DRAMPWROK topology



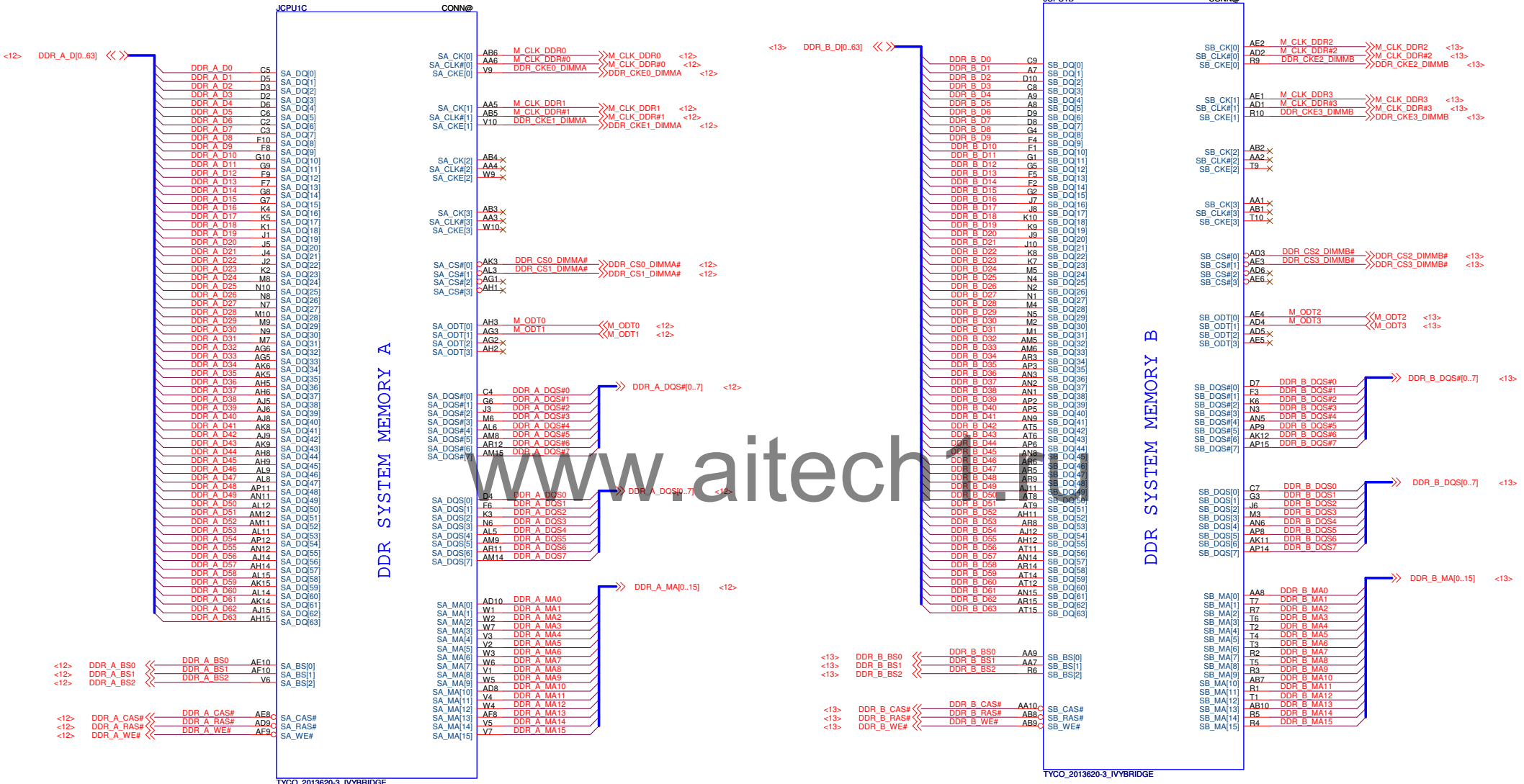
The resistor for HOOK2 should be placed such that the stub is very small on CFG0 net



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Ivy/Sandy Bridge (2/6)	
LA-7902P	
Date: Wednesday, March 07, 2012	Sheet 7 of 61

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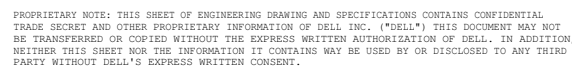
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Title
Ivy/Sandy Bridge (3/6)

Size Document Number
LA-7902P

Date: Wednesday, March 07, 2012 Sheet 8 of 61 Rev 1.0

Date: Wednesday, March 07, 2012 Sheet 9 of 61



POWER



GRAPHICS

DDR3 -1.5V RAILS

1.8V RAIL



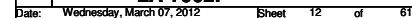
CONN@

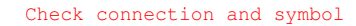
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Ivy/Sandy Bridge (6/6)

Date: Wednesday, March 07, 2012 Sheet 11 of 61

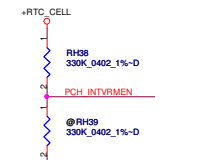




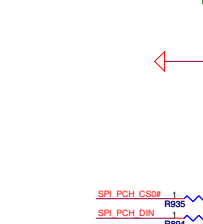
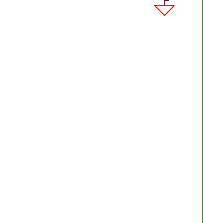
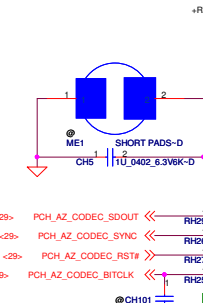


Date: Wednesday, March 07, 2012 Sheet 13 of 61

CMOS CLR1		CMOS setting	
Shunt	Clear CMOS	Shunt	Clear ME RTC Registers
Open	Keep CMOS	Open	Keep ME RTC Registers



INTVRMEN - Integrated SUS
1.1V VRM Enable
High - Enable Internal VRs
Low - Enable External VRs



On Die PLL VR is supplied by 1.5V when sampled high, 1.8 V when sampled low

On Die PLL VR is supplied by 1.5V when sampled high, 1.8 V when sampled low

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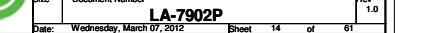
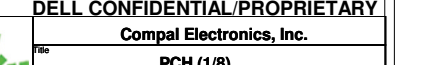
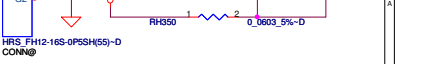
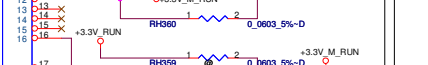
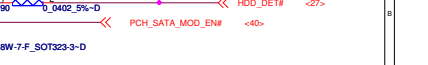
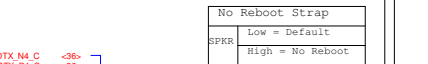
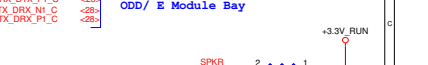
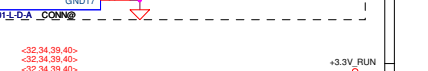
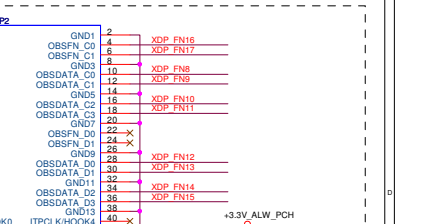
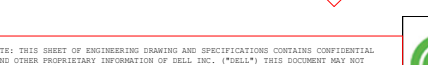
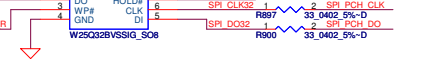
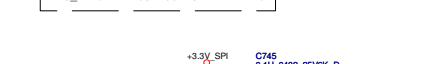
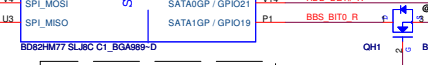
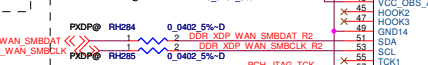
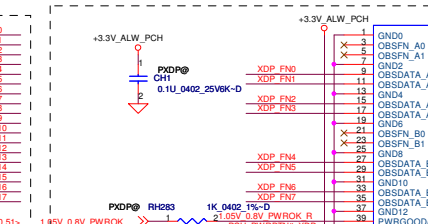
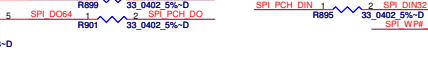
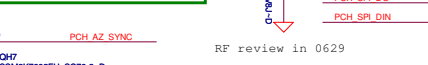
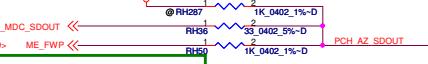
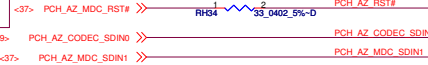
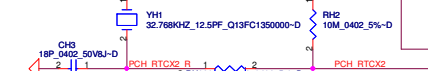
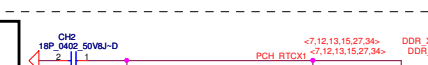
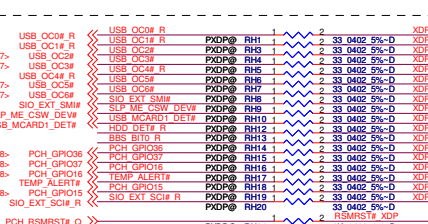
On Die PLL VR is supplied by 1.5V when sampled high, 1.8 V when sampled low

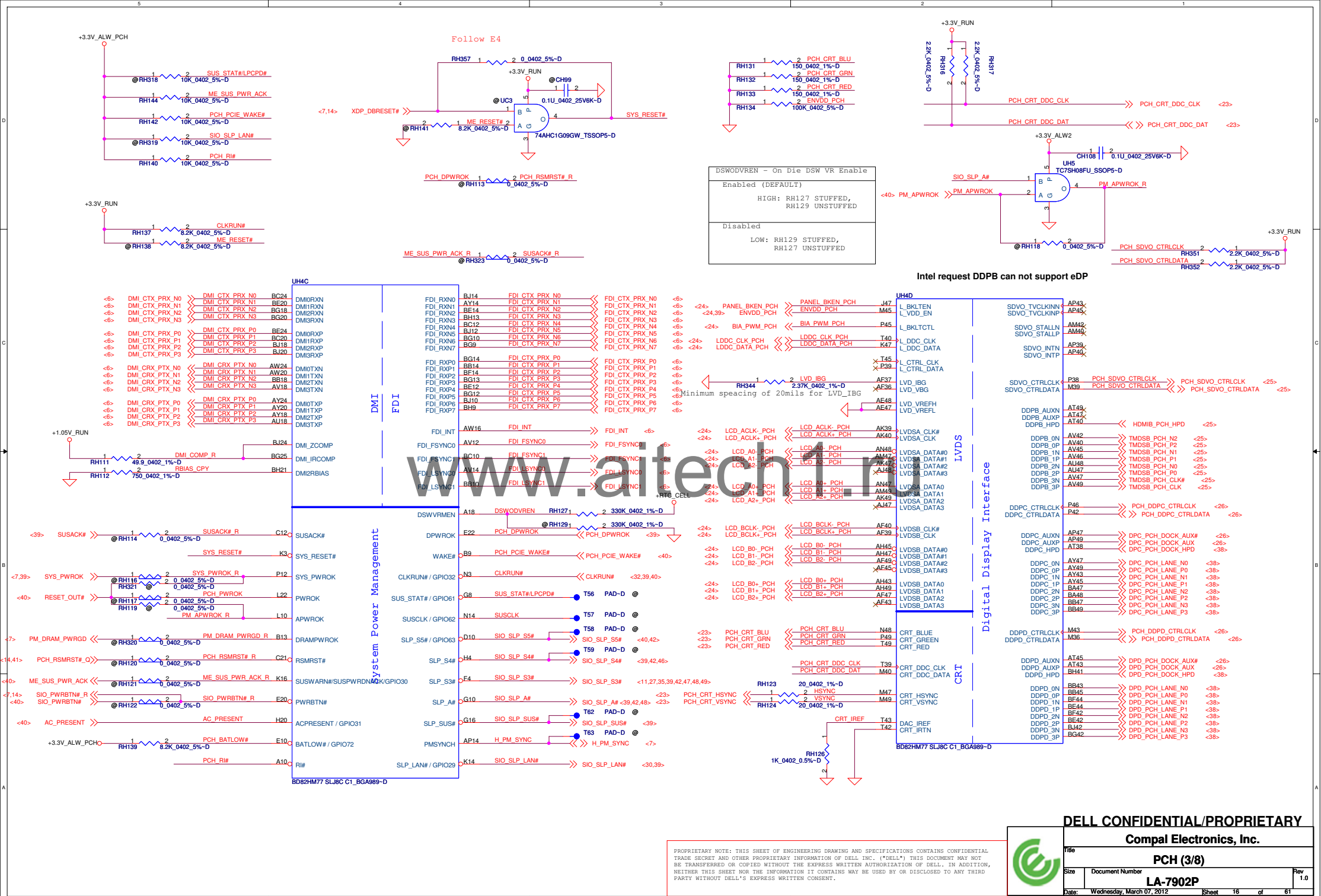
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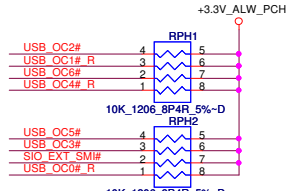
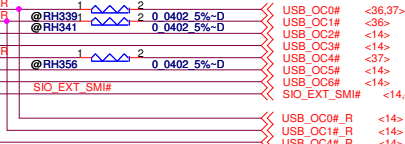
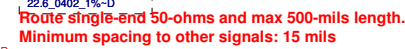
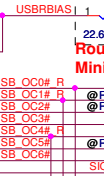
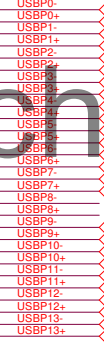
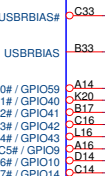
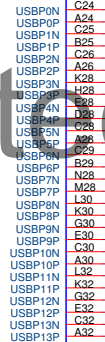
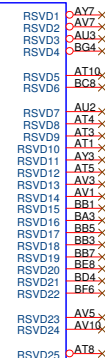
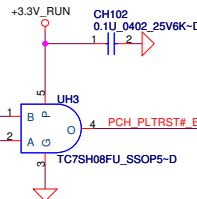
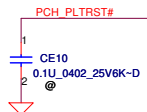
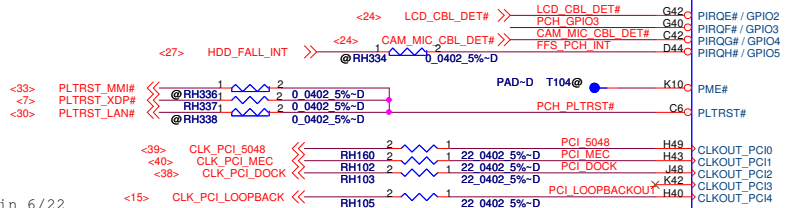
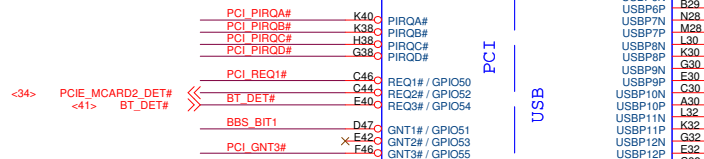
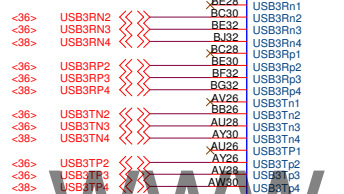
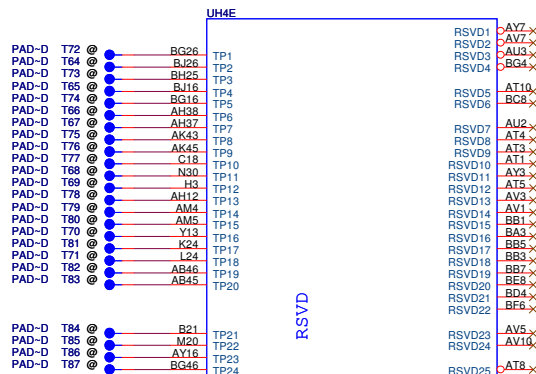
On Die PLL VR is supplied by 1.5V when sampled high, 1.8 V when sampled low

On Die PLL VR is supplied by 1.5V when sampled high, 1.8 V when sampled low

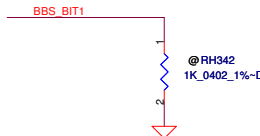
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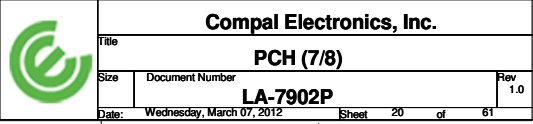






Boot BIOS Strap		
BBS_BIT1	SATA_SLDP (BBS_BIT0)	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI





UH4H		
H5	VSS[0]	
AA17	VSS[1]	VSS[80] AK38
AA2	VSS[2]	VSS[81] AK4
AA3	VSS[3]	VSS[82] AK46
AA33	VSS[4]	VSS[83] AK8
AA34	VSS[5]	VSS[84] AL16
AB11	VSS[6]	VSS[85] AL17
AB14	VSS[7]	VSS[86] AL19
AB39	VSS[8]	VSS[87] AL2
AB4	VSS[9]	VSS[88] AL21
AB43	VSS[10]	VSS[89] AL23
AB5	VSS[11]	VSS[90] AL26
AB7	VSS[12]	VSS[91] AL27
AC19	VSS[13]	VSS[92] AL31
AC2	VSS[14]	VSS[93] AL34
AC21	VSS[15]	VSS[94] AL48
AC24	VSS[16]	VSS[95] AM11
AC33	VSS[17]	VSS[96] AM14
AC34	VSS[18]	VSS[97] AM36
AC48	VSS[19]	VSS[98] AM39
AD10	VSS[20]	VSS[99] AM43
AD11	VSS[21]	VSS[100] AM45
AD12	VSS[22]	VSS[101] AM46
AD13	VSS[23]	VSS[102] AM7
AD19	VSS[24]	VSS[103] AN2
AD24	VSS[25]	VSS[104] AN29
AD26	VSS[26]	VSS[105] AN3
AD27	VSS[27]	VSS[106] AN31
AD33	VSS[28]	VSS[107] AP12
AD34	VSS[29]	VSS[108] AP19
AD36	VSS[30]	VSS[109] AP28
AD37	VSS[31]	VSS[110] AP32
AD38	VSS[32]	VSS[111] AP38
AD39	VSS[33]	VSS[112] AP4
AD4	VSS[34]	VSS[113] AP24
AD40	VSS[35]	VSS[114] AP46
AD42	VSS[36]	VSS[115] AP8
AD43	VSS[37]	VSS[116] AR2
AD45	VSS[38]	VSS[117] AR48
AD46	VSS[39]	VSS[118] AT11
AD8	VSS[40]	VSS[119] AT13
AE2	VSS[41]	VSS[120] AT18
AE3	VSS[42]	VSS[121] AT22
AF10	VSS[43]	VSS[122] AT26
AF12	VSS[44]	VSS[123] AT28
AD14	VSS[45]	VSS[124] AT33
AD16	VSS[46]	VSS[125] AT39
AF16	VSS[47]	VSS[126] AT42
AF19	VSS[48]	VSS[127] AT46
AF24	VSS[49]	VSS[128] AU24
AF26	VSS[50]	VSS[129] AU30
AF27	VSS[51]	VSS[130] AV16
AF29	VSS[52]	VSS[131] AV20
AF31	VSS[53]	VSS[132] AV24
AF38	VSS[54]	VSS[133] AV30
AF4	VSS[55]	VSS[134] AV38
AF42	VSS[56]	VSS[135] AV43
AF46	VSS[57]	VSS[136] AV8
AF5	VSS[58]	VSS[137] AW14
AF7	VSS[59]	VSS[138] AW18
AF8	VSS[60]	VSS[139] AW22
AG19	VSS[61]	VSS[140] AW26
AG2	VSS[62]	VSS[141] AW28
AG31	VSS[63]	VSS[142] AW32
AG48	VSS[64]	VSS[143] AW36
AH11	VSS[65]	VSS[144] AW40
AH3	VSS[66]	VSS[145] AW48
AH36	VSS[67]	VSS[146] AW11
AH39	VSS[68]	VSS[147] AY12
AH40	VSS[69]	VSS[148] AY22
AH42	VSS[70]	VSS[149] AY28
AH46	VSS[71]	VSS[150]
AH7	VSS[72]	VSS[151]
AJ19	VSS[73]	VSS[152]
AJ21	VSS[74]	VSS[153]
AJ24	VSS[75]	VSS[154]
AJ33	VSS[76]	VSS[155]
AJ34	VSS[77]	VSS[156]
AK12	VSS[78]	VSS[157]
AK3	VSS[79]	VSS[158]

BD82HM77 SLJ8C C1_BGA989-D

UH4I		
AY4	VSS[159]	VSS[259] H46
AY42	VSS[160]	VSS[260] K18
AY46	VSS[161]	VSS[261] K26
AY8	VSS[162]	VSS[262] K39
B11	VSS[163]	VSS[263] K46
B15	VSS[164]	VSS[264] K7
B19	VSS[165]	VSS[265] L18
B23	VSS[166]	VSS[266] L2
B27	VSS[167]	VSS[267] L20
B31	VSS[168]	VSS[268] L26
B35	VSS[169]	VSS[269] L28
B39	VSS[170]	VSS[270] L48
B7	VSS[171]	VSS[271] M12
F45	VSS[172]	VSS[272] P16
BB12	VSS[173]	VSS[273] M18
BB16	VSS[174]	VSS[274] M22
BB20	VSS[175]	VSS[275] M24
BB22	VSS[176]	VSS[276] M30
BB24	VSS[177]	VSS[277] M32
BB28	VSS[178]	VSS[278] M34
BB30	VSS[179]	VSS[279] M38
BB38	VSS[180]	VSS[280] M4
BB4	VSS[181]	VSS[281] M42
BB46	VSS[182]	VSS[282] M46
BC14	VSS[183]	VSS[283] M8
BC18	VSS[184]	VSS[284] N18
BC2	VSS[185]	VSS[285] P30
BC22	VSS[186]	VSS[286] P47
BC26	VSS[187]	VSS[287] P11
BC32	VSS[188]	VSS[288] P18
BC34	VSS[189]	VSS[289] T33
BC36	VSS[190]	VSS[290] P40
BC40	VSS[191]	VSS[291] P43
BC42	VSS[192]	VSS[292] P47
BC48	VSS[193]	VSS[293] P7
BD46	VSS[194]	VSS[294] R2
BD5	VSS[195]	VSS[295] R48
BE22	VSS[196]	VSS[296] T12
BE26	VSS[197]	VSS[297] T31
BE40	VSS[198]	VSS[298] T37
BE10	VSS[199]	VSS[299] T4
BE12	VSS[200]	VSS[300] W34
BF16	VSS[201]	VSS[301] T46
BF20	VSS[202]	VSS[302] T47
BF22	VSS[203]	VSS[303] T8
BF24	VSS[204]	VSS[304] V11
BF26	VSS[205]	VSS[305] V17
BF28	VSS[206]	VSS[306] V26
BD3	VSS[207]	VSS[307] V27
BF30	VSS[208]	VSS[308] V29
BF38	VSS[209]	VSS[309] V34
BF40	VSS[210]	VSS[310] V36
BF42	VSS[211]	VSS[311] V39
BF44	VSS[212]	VSS[312] V43
BF46	VSS[213]	VSS[313] V7
BF48	VSS[214]	VSS[314] W17
BF49	VSS[215]	VSS[315] W19
BF49	VSS[216]	VSS[316] W2
BF49	VSS[217]	VSS[317] W27
BF49	VSS[218]	VSS[318] W48
BF49	VSS[219]	VSS[319] Y12
BF49	VSS[220]	VSS[320] Y38
BF49	VSS[221]	VSS[321] Y4
BF49	VSS[222]	VSS[322] Y42
BF49	VSS[223]	VSS[323] Y46
BF49	VSS[224]	VSS[324] Y8
BF49	VSS[225]	VSS[325] BG29
BF49	VSS[226]	VSS[326] N24
BF49	VSS[227]	VSS[327] AJ3
BF49	VSS[228]	VSS[328] B43
BF49	VSS[229]	VSS[329] BE10
BF49	VSS[230]	VSS[330] BG41
BF49	VSS[231]	VSS[331] G14
BF49	VSS[232]	VSS[332] T36
BF49	VSS[233]	VSS[333] BG22
BF49	VSS[234]	VSS[334] C22
BF49	VSS[235]	VSS[335] AP13
BF49	VSS[236]	VSS[336] M14
BF49	VSS[237]	VSS[337] AP3
BF49	VSS[238]	VSS[338] AP1
BF49	VSS[239]	VSS[339] BE16
BF49	VSS[240]	VSS[340] BC16
BF49	VSS[241]	VSS[341] BG28
BF49	VSS[242]	VSS[342] BJ28
BF49	VSS[243]	VSS[343]
BF49	VSS[244]	VSS[344]
BF49	VSS[245]	VSS[345]
BF49	VSS[246]	VSS[346]
BF49	VSS[247]	VSS[347]
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BF49	VSS[251]	VSS[351]
BF49	VSS[252]	VSS[352]
BF49	VSS[253]	
BF49	VSS[254]	
BF49	VSS[255]	
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BF49	VSS[258]	

BD82HM77 SLJ8C C1_BGA989-D

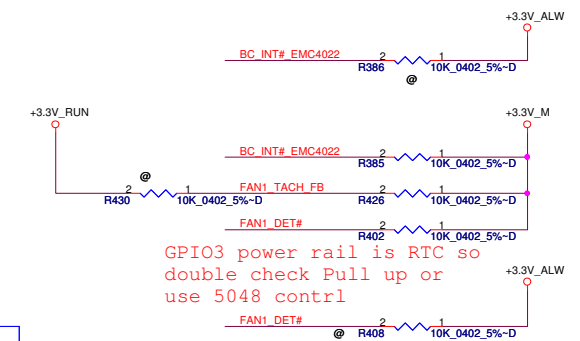
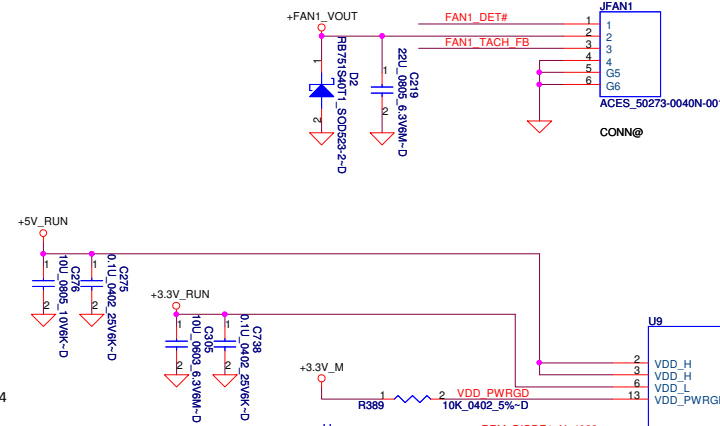
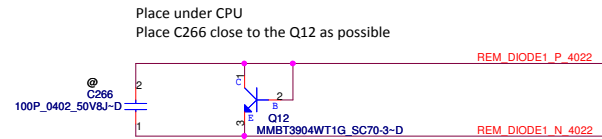
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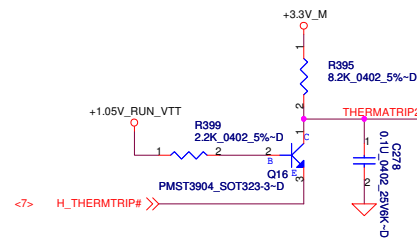
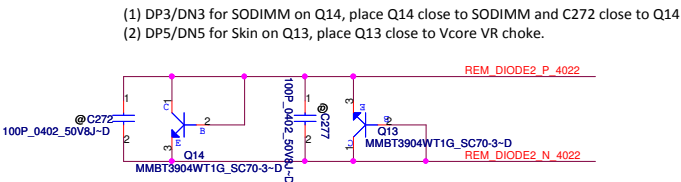
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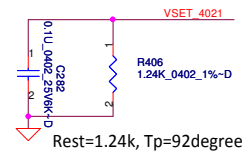
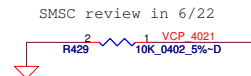
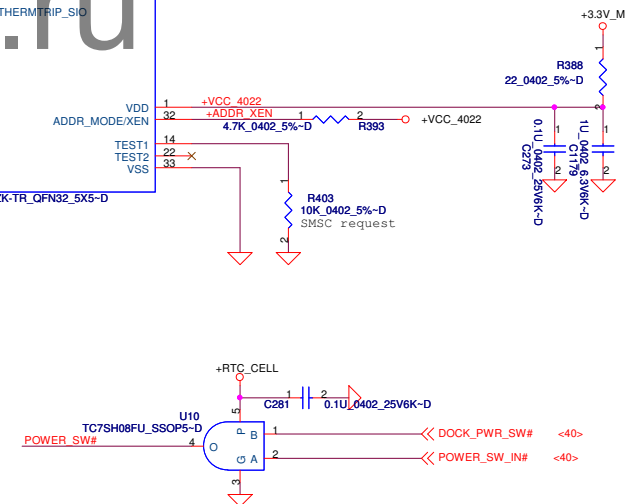
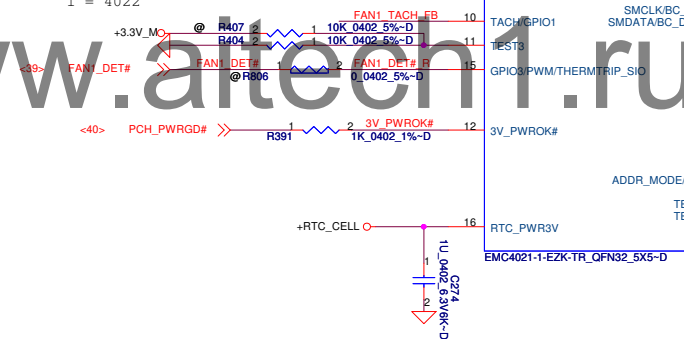
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PCH (8/8)			
Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	21 of 61



GPI03 power rail is RTC so
double check Pull up or
use 5048 contrl



0 = 4021 SMSC request
1 = 4022



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FAN & Thermal Sensor

LA-7902P

1.0

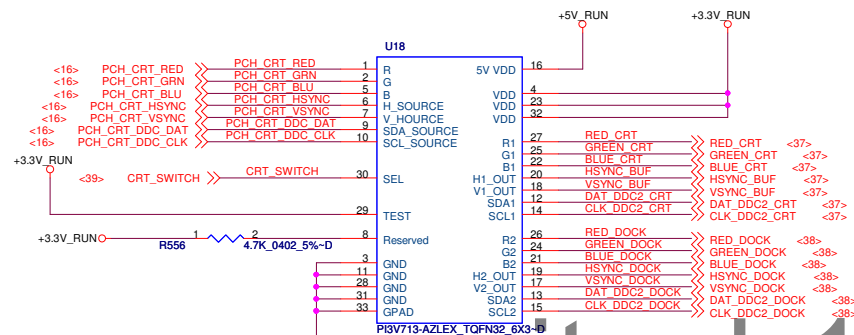
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Sheet 22 of 61

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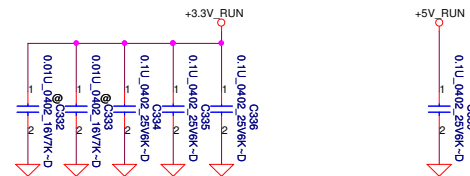


SW for MB/DOCK




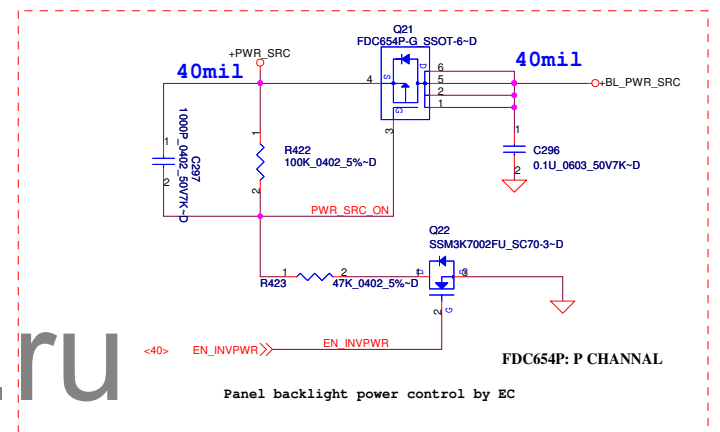
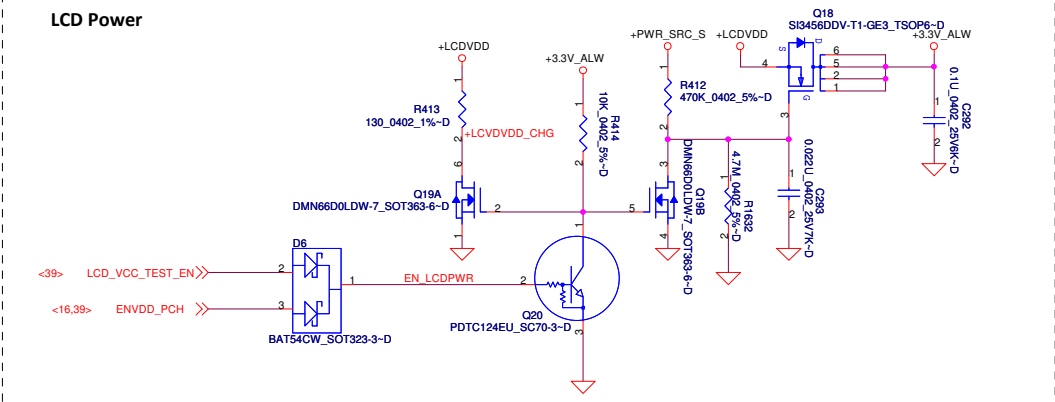
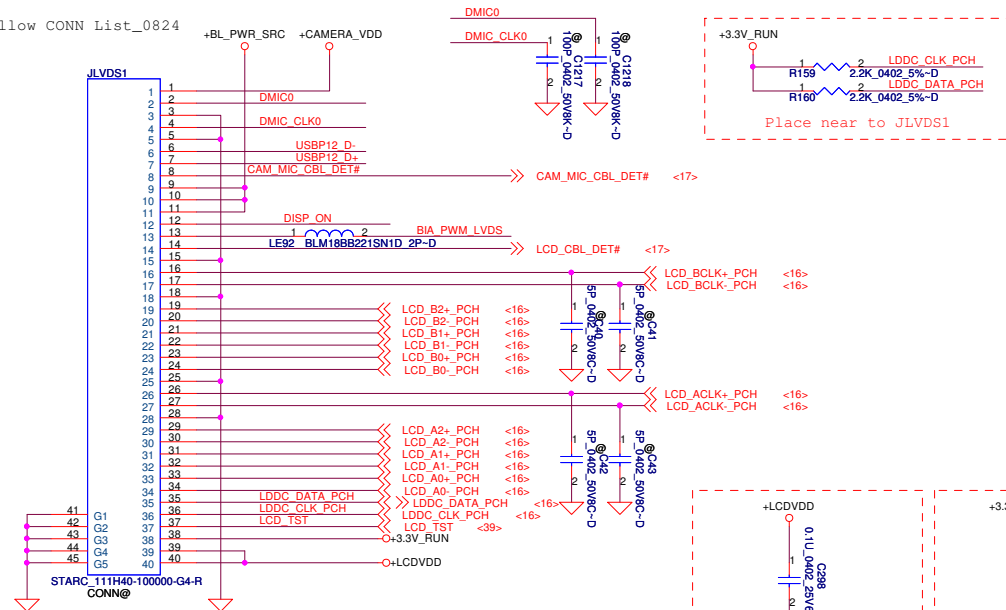
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SEL1/SEL2	Chanel	Source
0	A=B1	MB
1	A=B2	APR/SPR

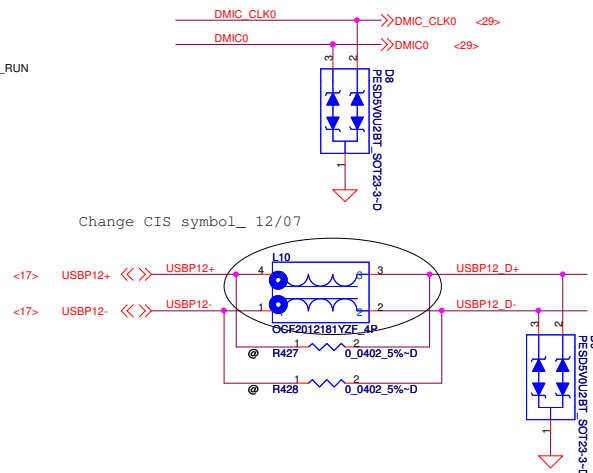
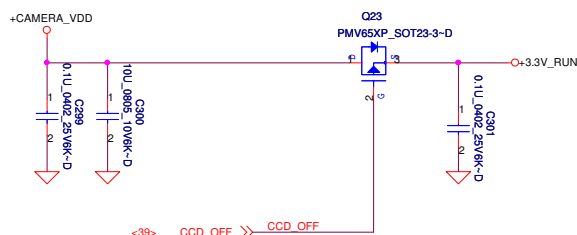


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Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	23 of 61



For Webcam



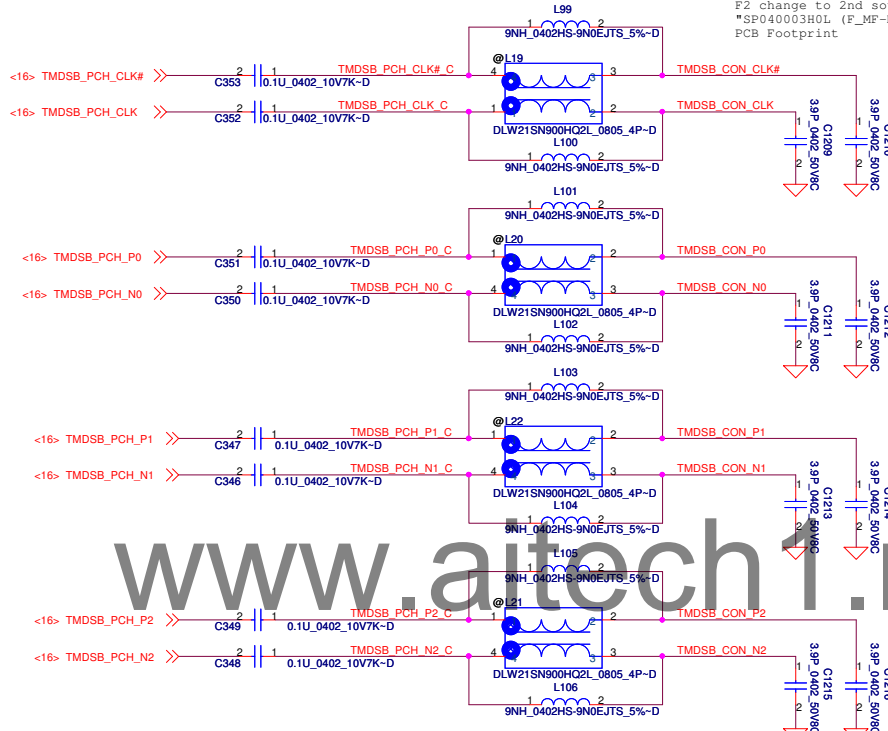
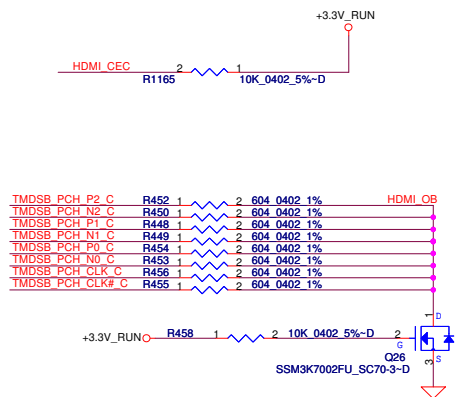
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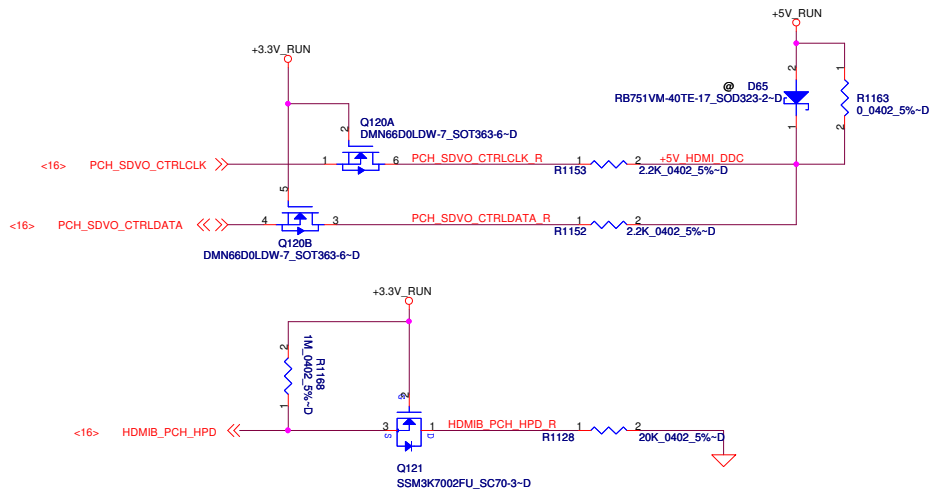
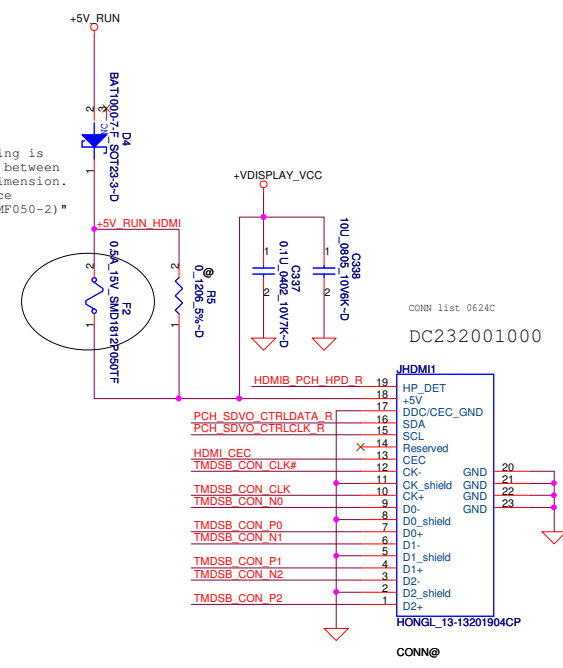


Compal Electronics, Inc.

Title			
LVDS & CAM Conn			
Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	24 of 61



Note:
AOI found open soldering is
due to the difference between
Main and 2nd on PAD dimension.
F2 change to 2nd source
"SP040003H0L (F_MF-MSMF050-2)"
PCB Footprint



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Part Number	Description
RO0000002HM	HDMI W/Logo:RO0000002HM

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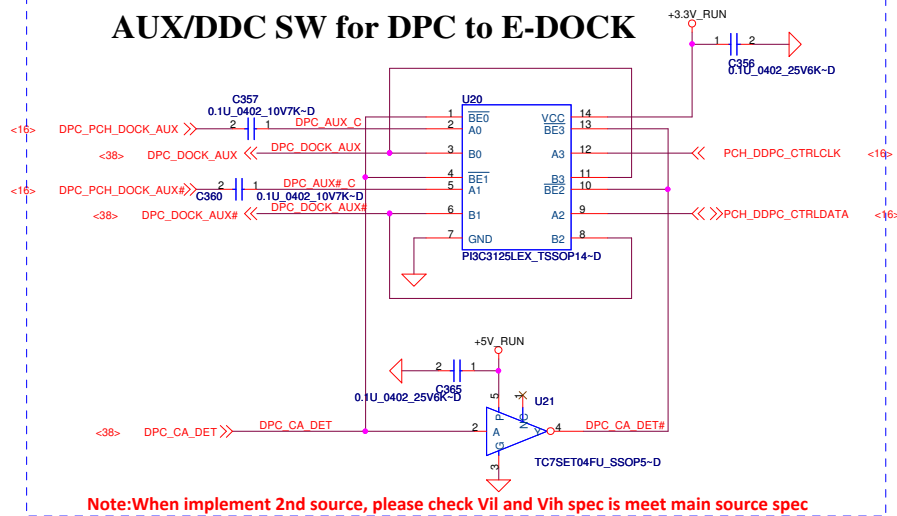
Compal Electronics, Inc.

HDMI port

LA-7902P

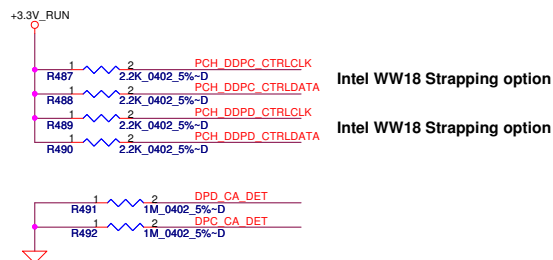
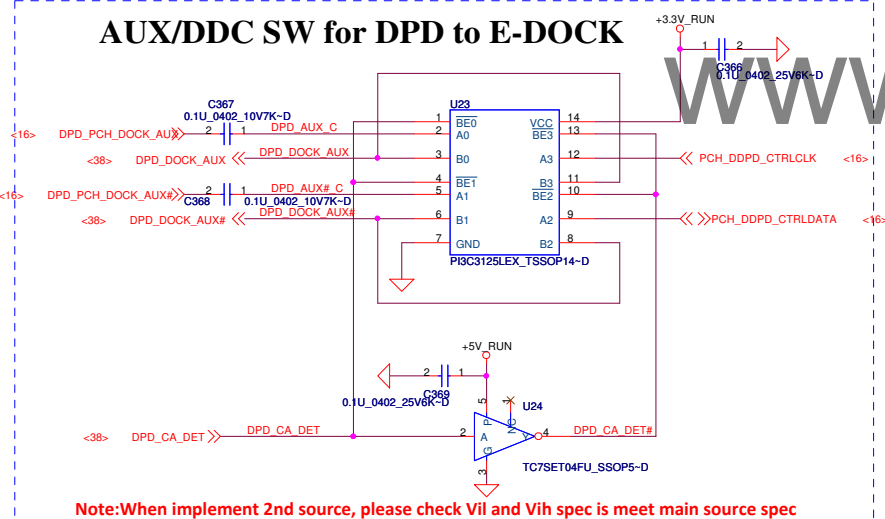
Date: Wednesday, March 07, 2012 Sheet 25 of 61

AUX/DDC SW for DPC to E-DOCK



There is a new die for PI3C3125. Sample available on May.

AUX/DDC SW for DPD to E-DOCK

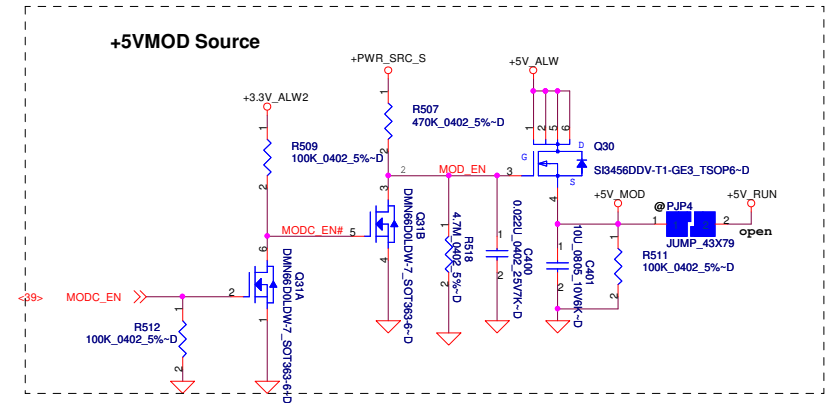
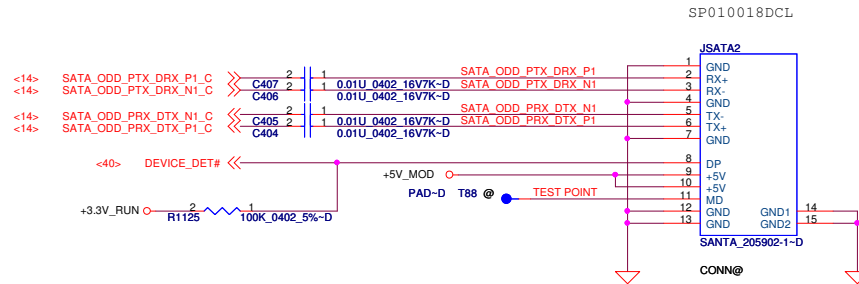
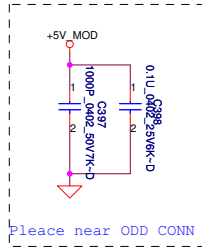


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Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	26 of 61

For ODD



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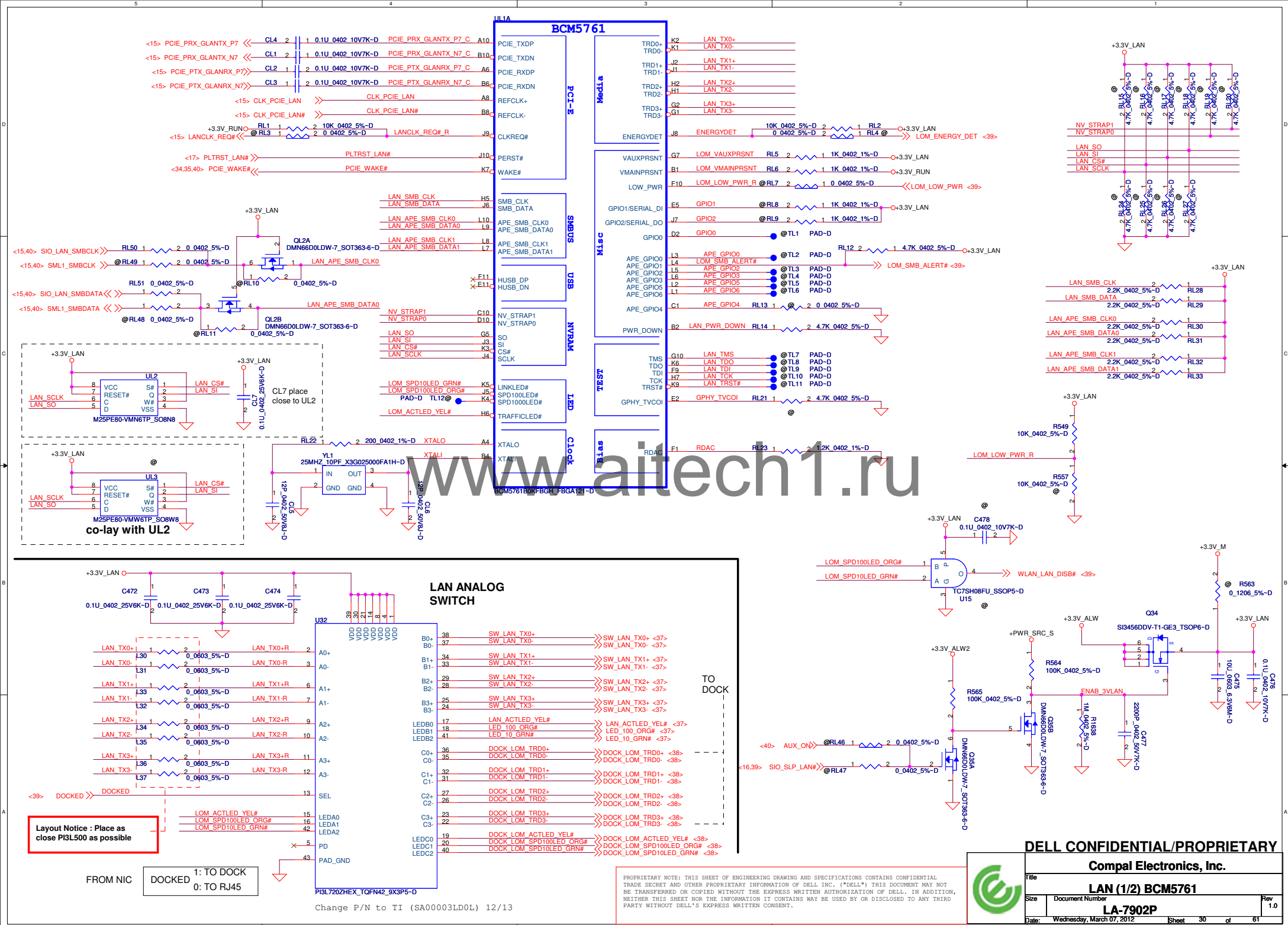
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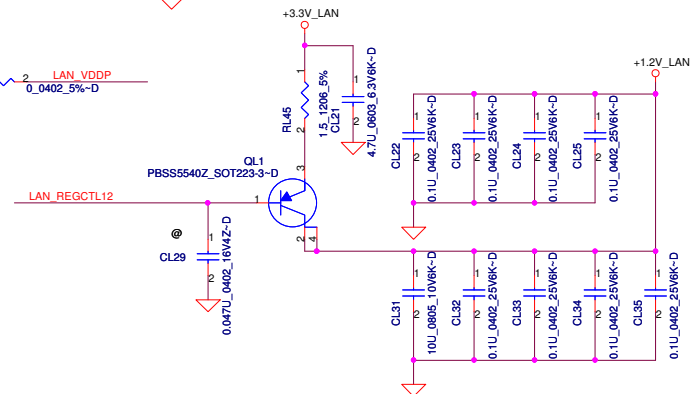
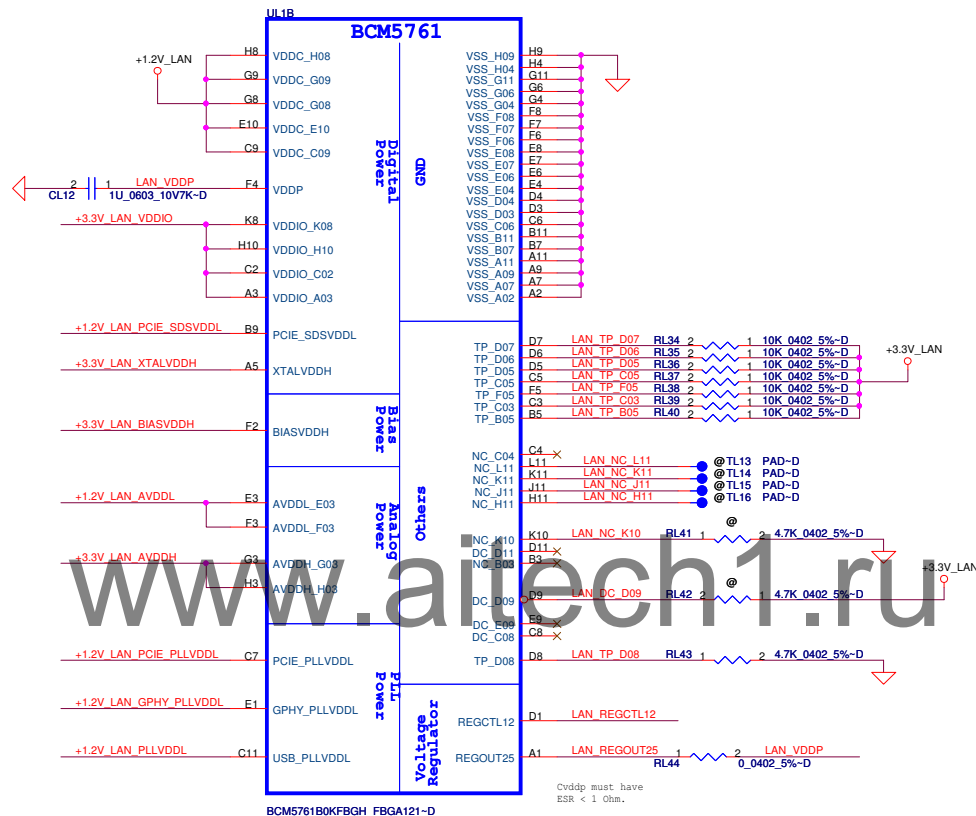
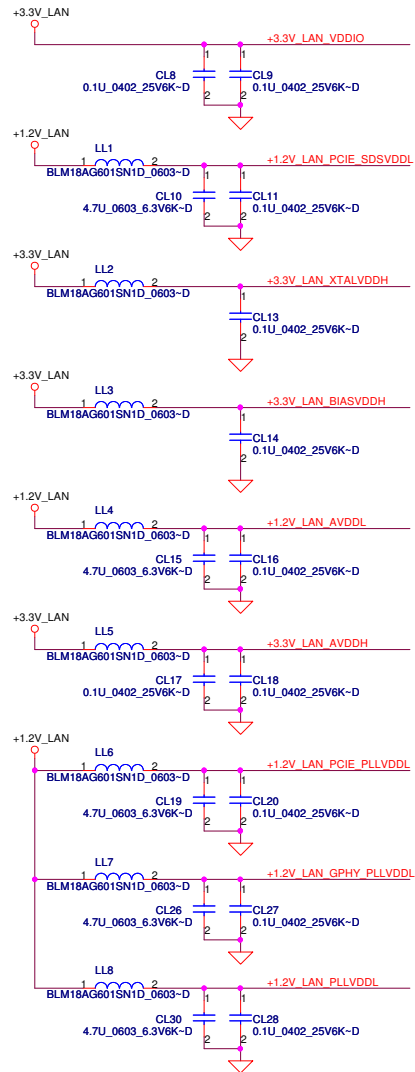
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Size	Document Number	Rev	
		1.0	
Date:	Wednesday, March 07, 2012	Sheet	28 of 61







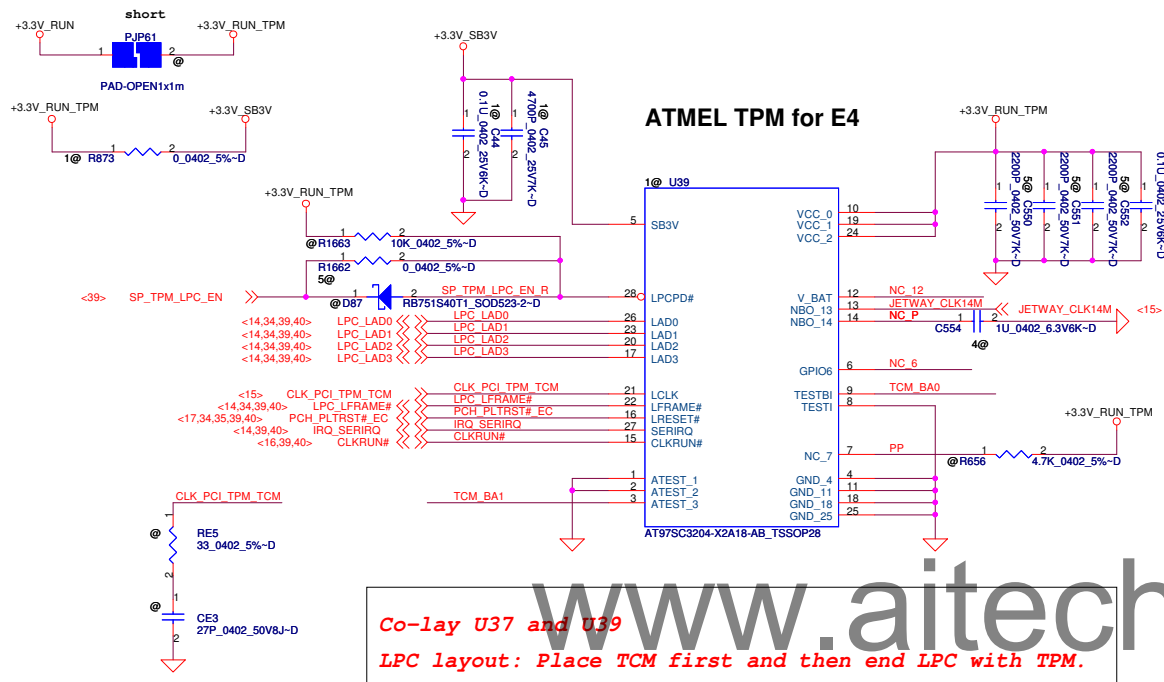
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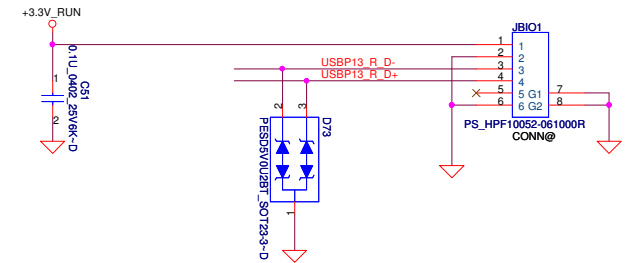
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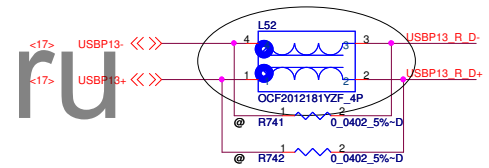
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Size			Rev 1.0		
Date			Wednesday, March 07, 2012		
Sheet			31 of 61		



Change CONN to pitch 1.0 _0815

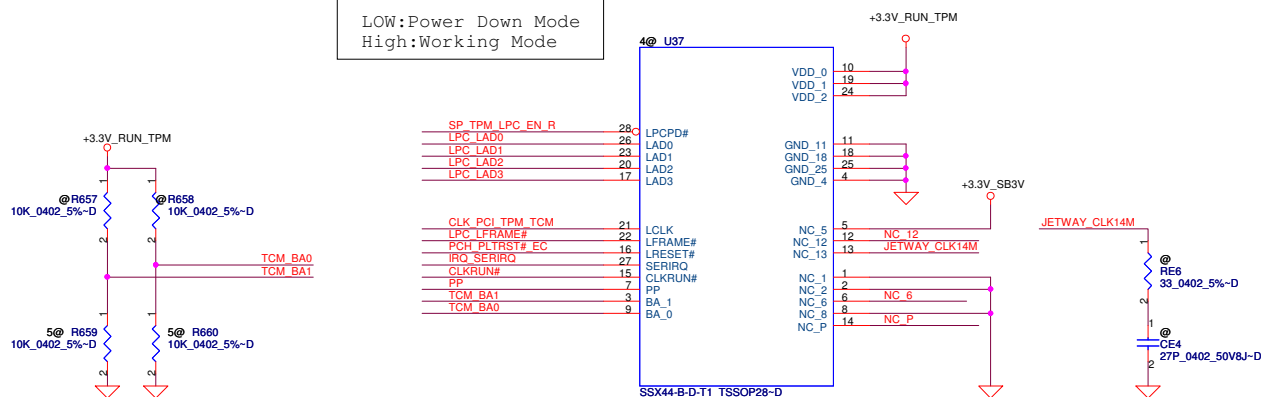


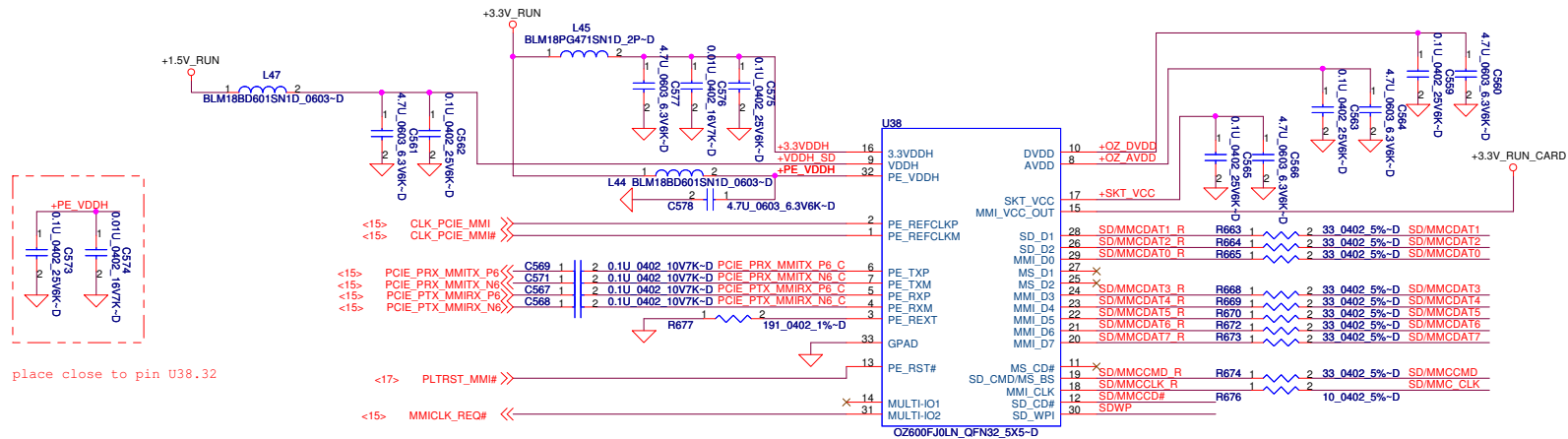
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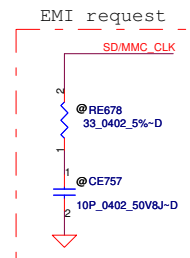
China TCM: NationZ & Jetway co-lay

LOW:Power Down Mode
High:Working Mode





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76 pin 2

CONN@

only for MMC/SD

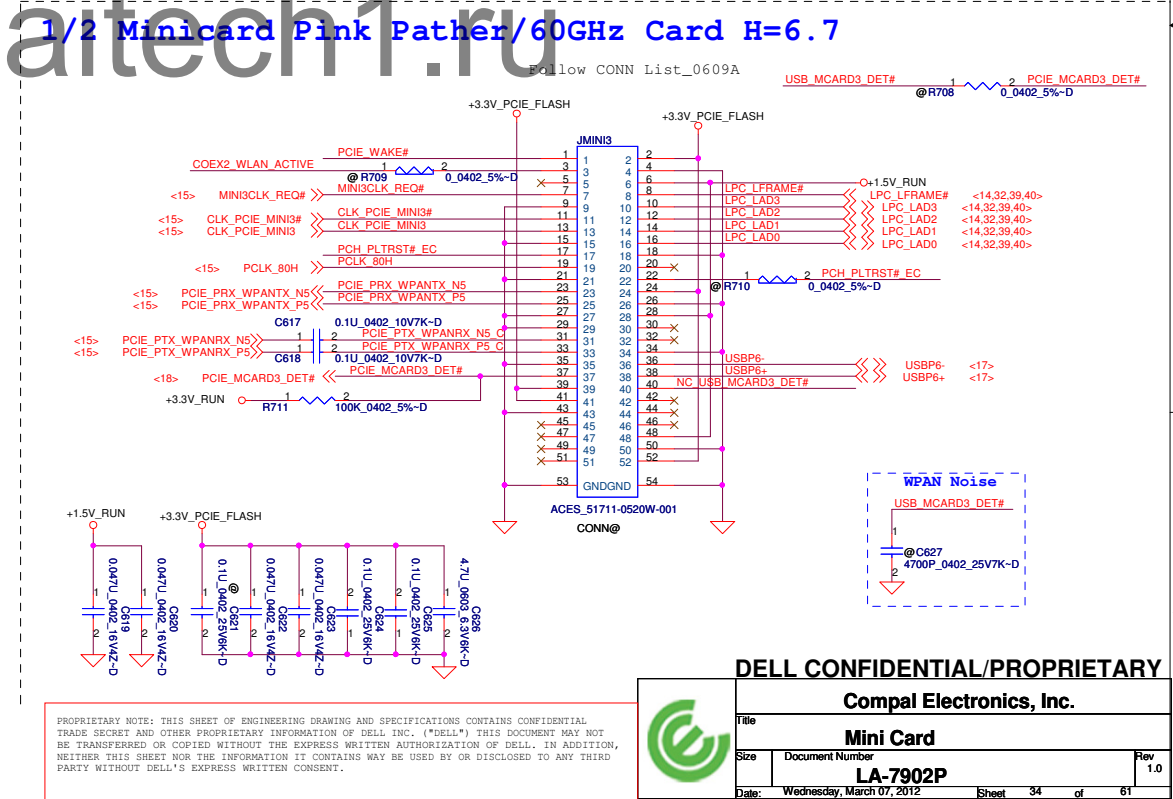
Sheet 33 of 61

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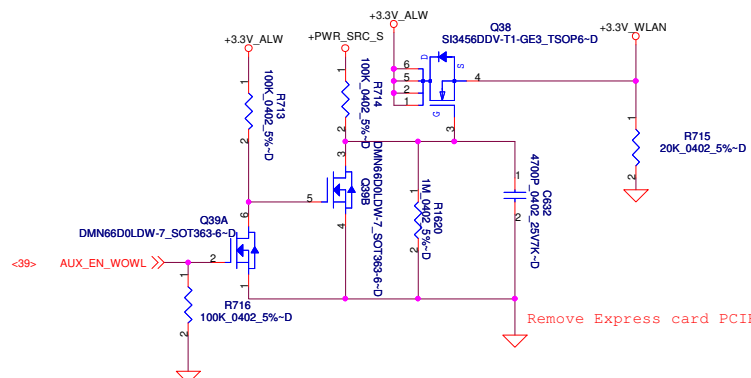


PWR Rail	Voltage Tolerance	Primary Power		Aux Power
		Peak	Normal	Normal
+3.3V	+9%	1000	750	
+3.3Vaux	+9%	330	250	250 (Wake enable) 5 (Not wake enable)
+1.5V	+5%	500	375	NA

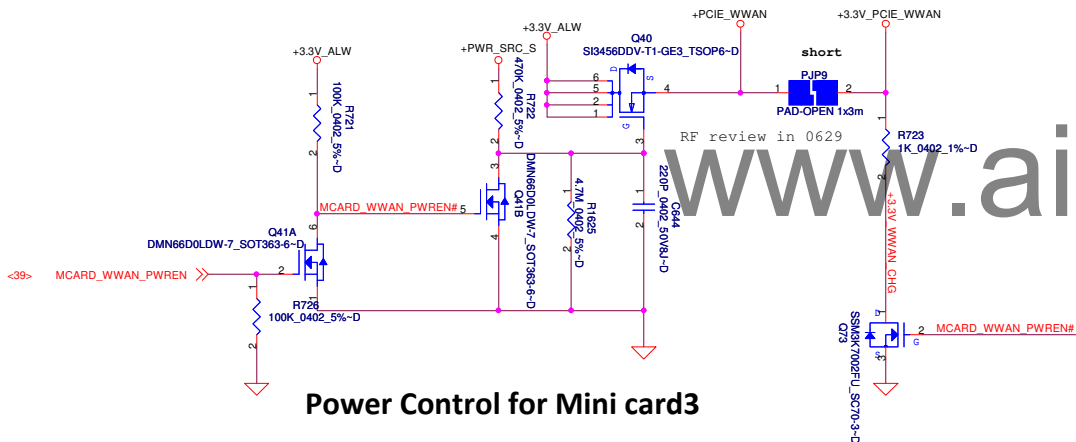


Date: Wednesday, March 07, 2012 Sheet 34 of 61

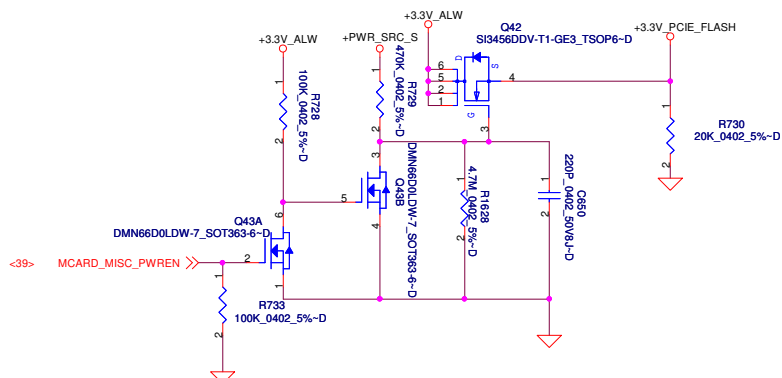
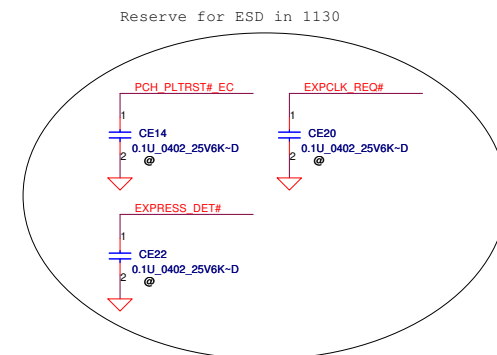
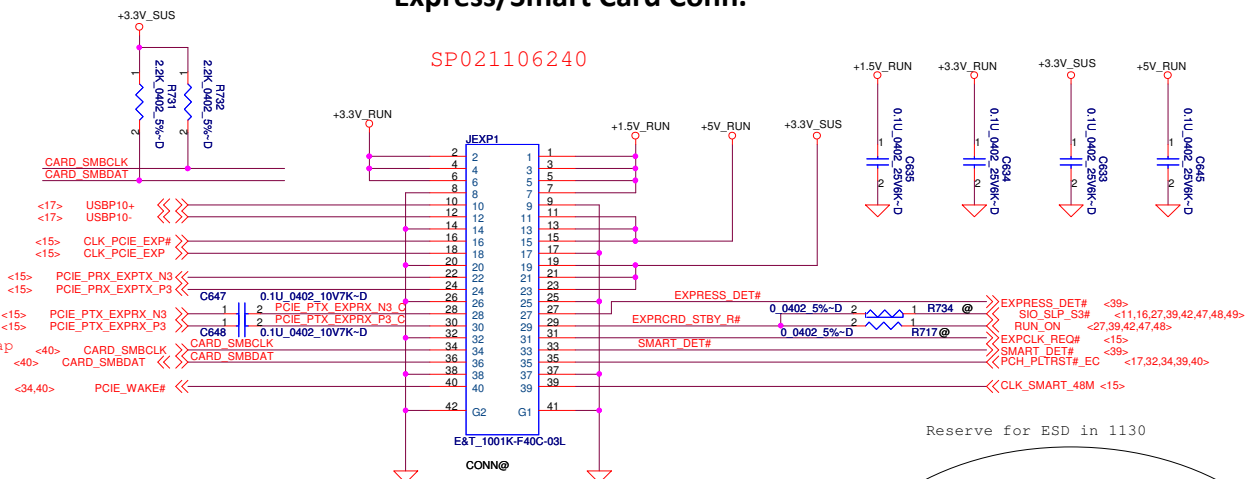
Power Control for Mini card2



Power Control for Mini card1



Power Control for Mini card3

**Express/Smart Card Conn.**

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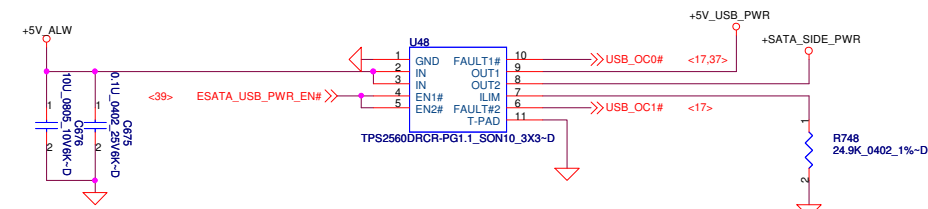
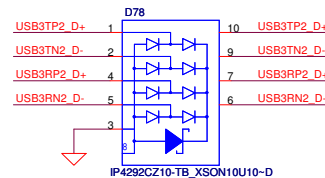
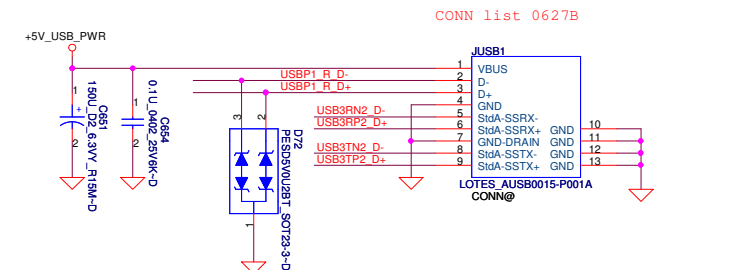
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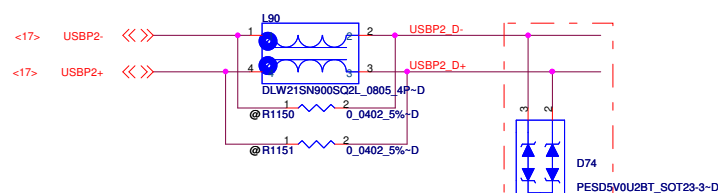
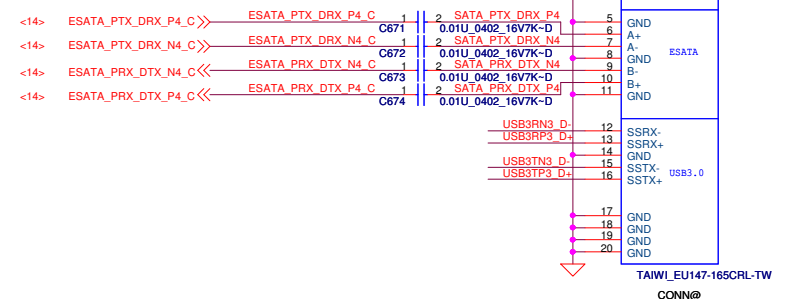
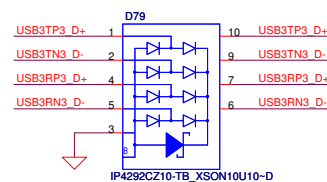
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Size	Document Number	Rev
	LA-7902P	1.0

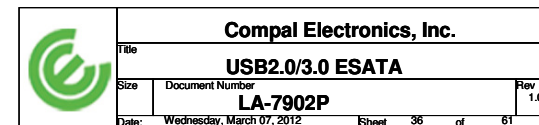
Date: Wednesday, March 07, 2012 Sheet 35 of 61



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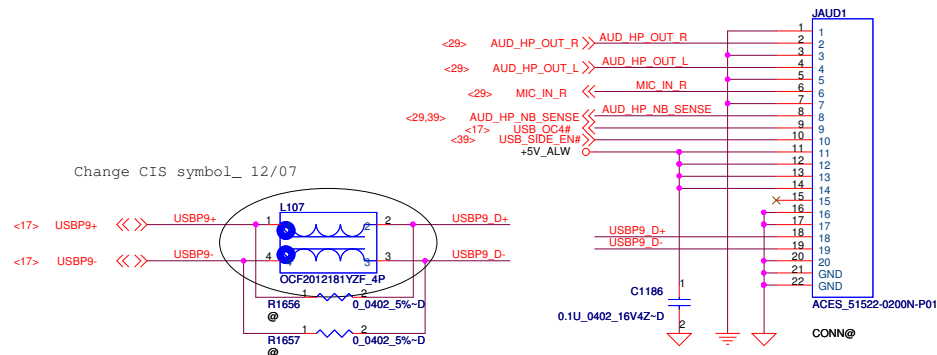
Place D74 close to JESATA1



AUDIO BOARD

Pitch=0.5

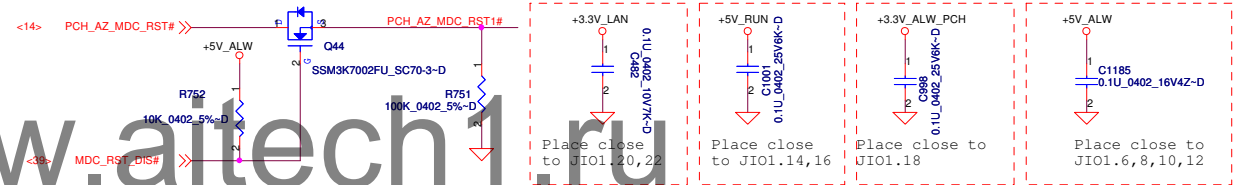
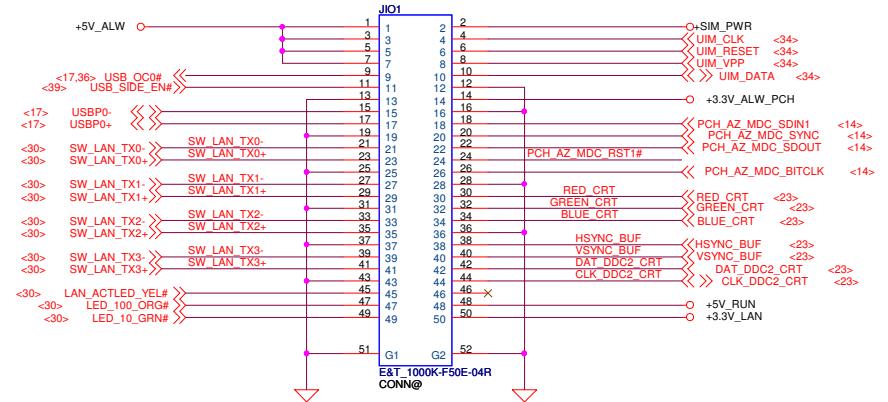
Follow CONN List_1130A



IO BOARD

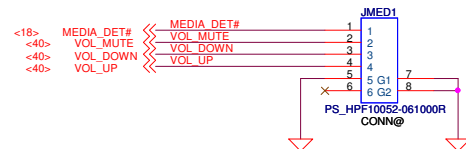
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Follow CONN List_0609A

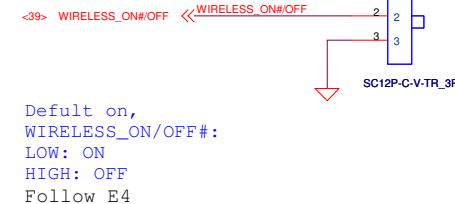


MEDIA BOARD

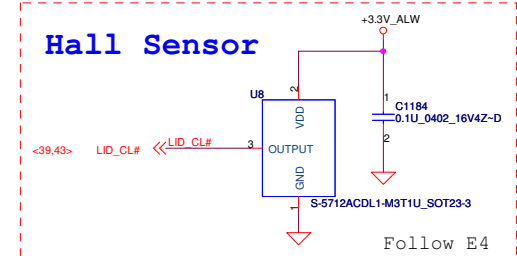
Follow CONN List_0621A



Sniffer Switch



Hall Sensor



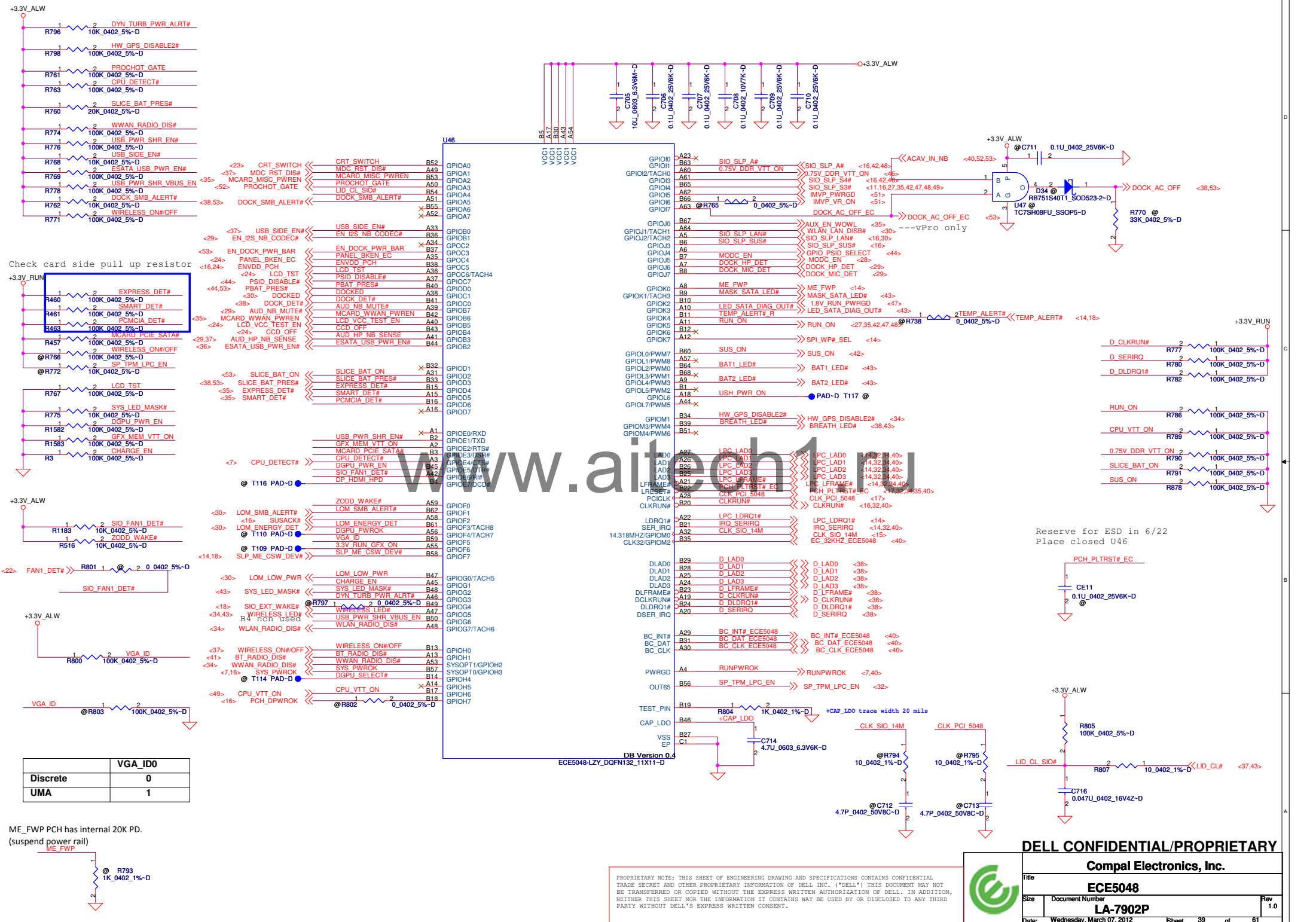
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IO/AUDIO/MEDIA/SNF			
Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	37 of 61



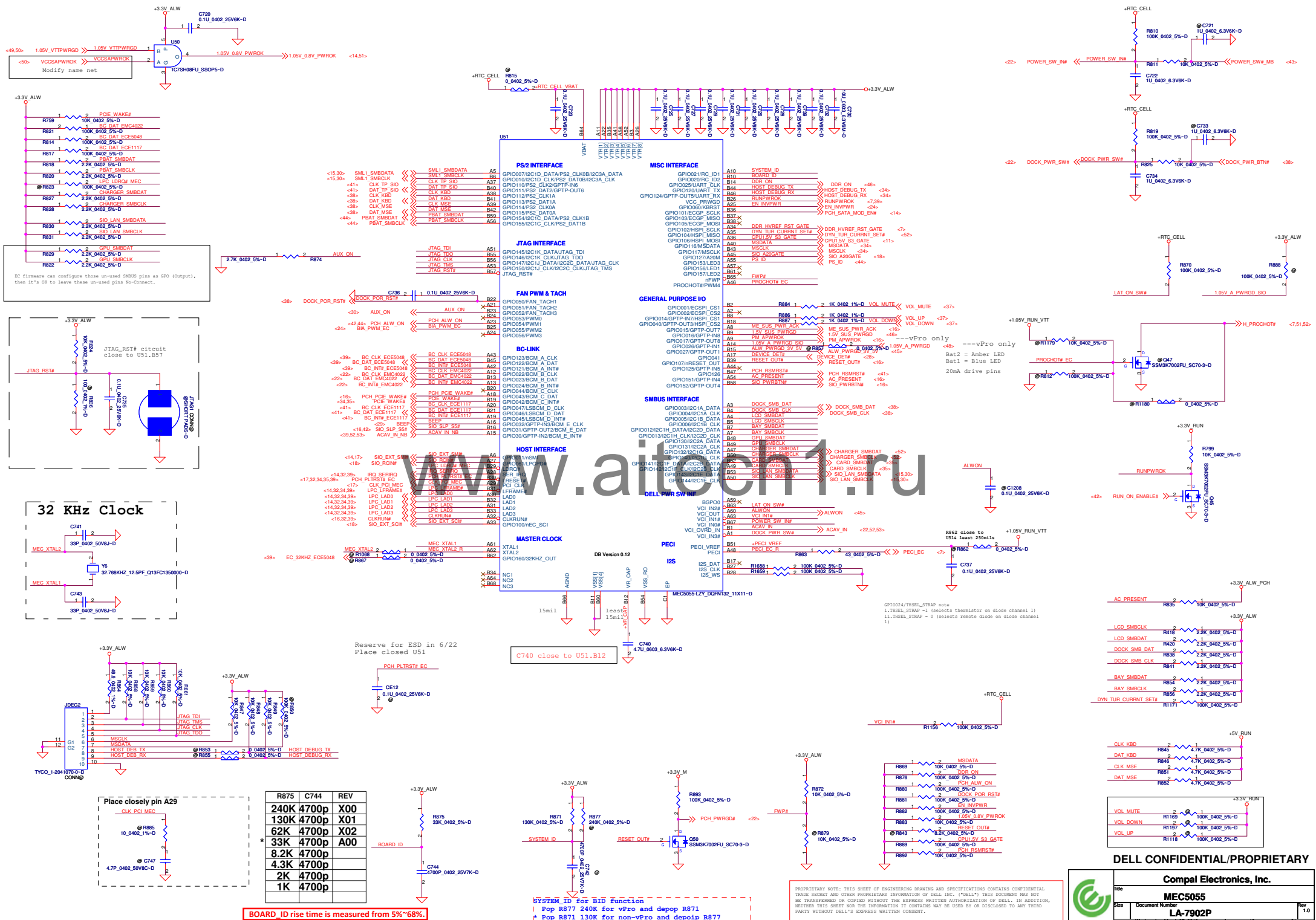
	VGA_ID0
Discrete	0
UMA	1

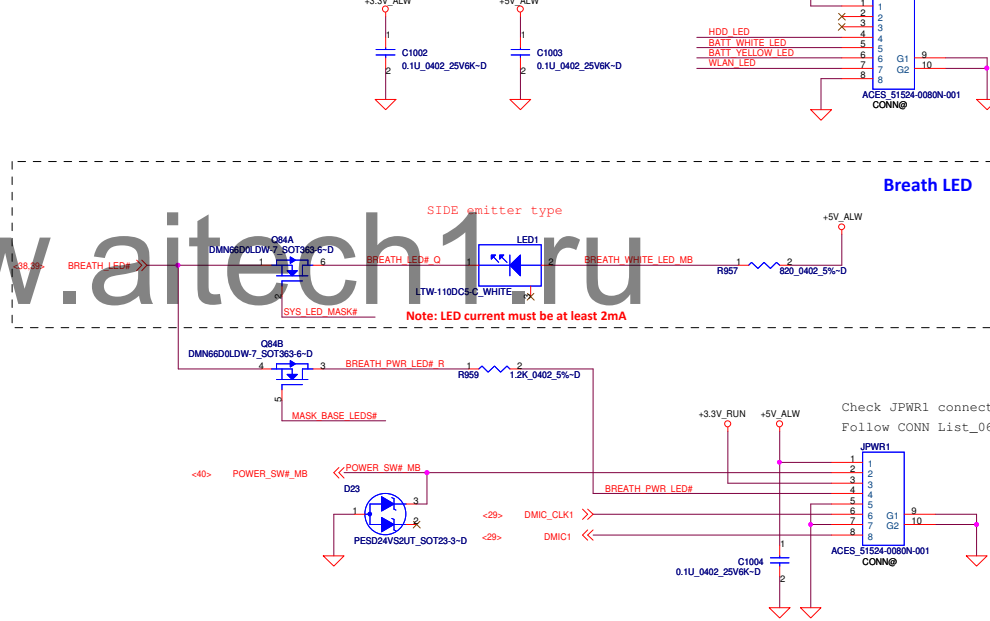
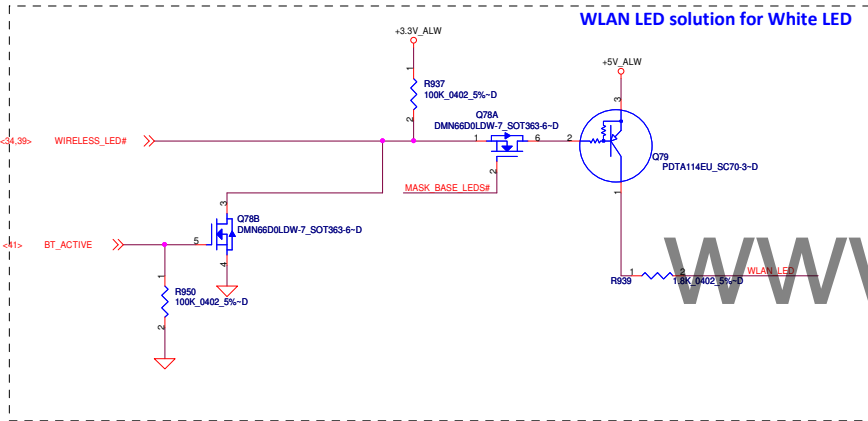
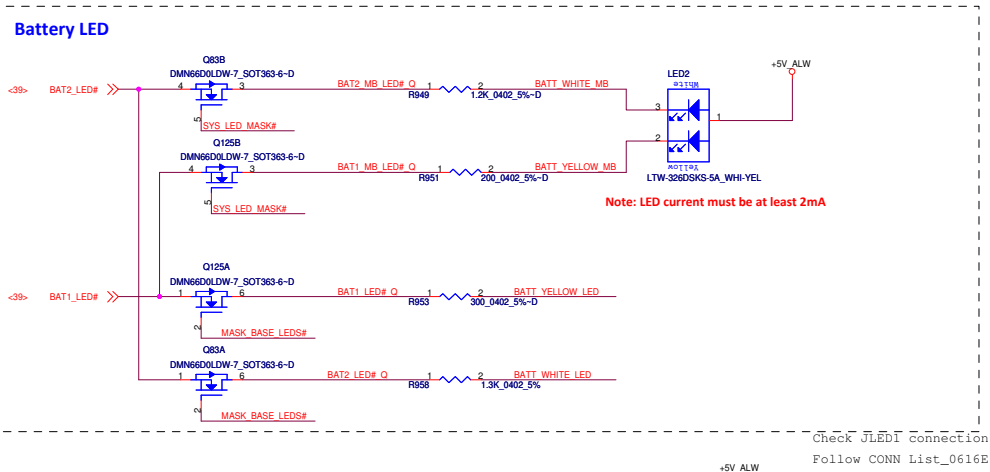
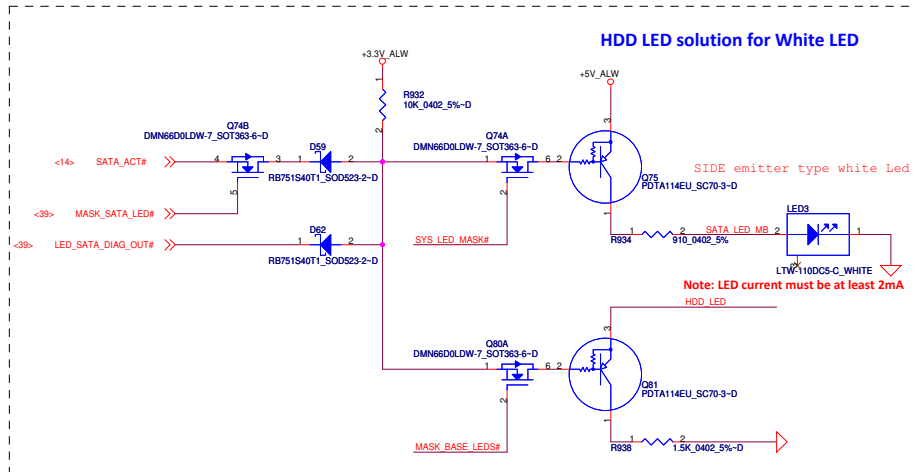
ME_FWP PCH has internal 20K PD.
(suspend power rail)

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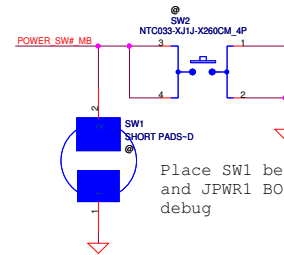
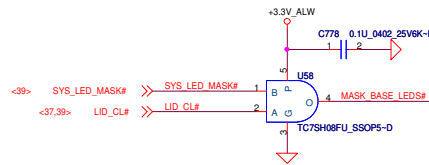
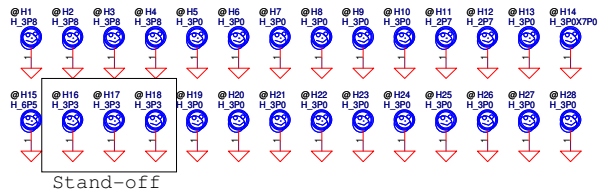
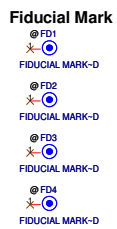


Compel Electronics, Inc.			
Title			
ECE5048			
Size	Document Number	Rev	
	LA-7902P	1.0	
Date:	Wednesday, March 07, 2012	Sheet	39 of 61





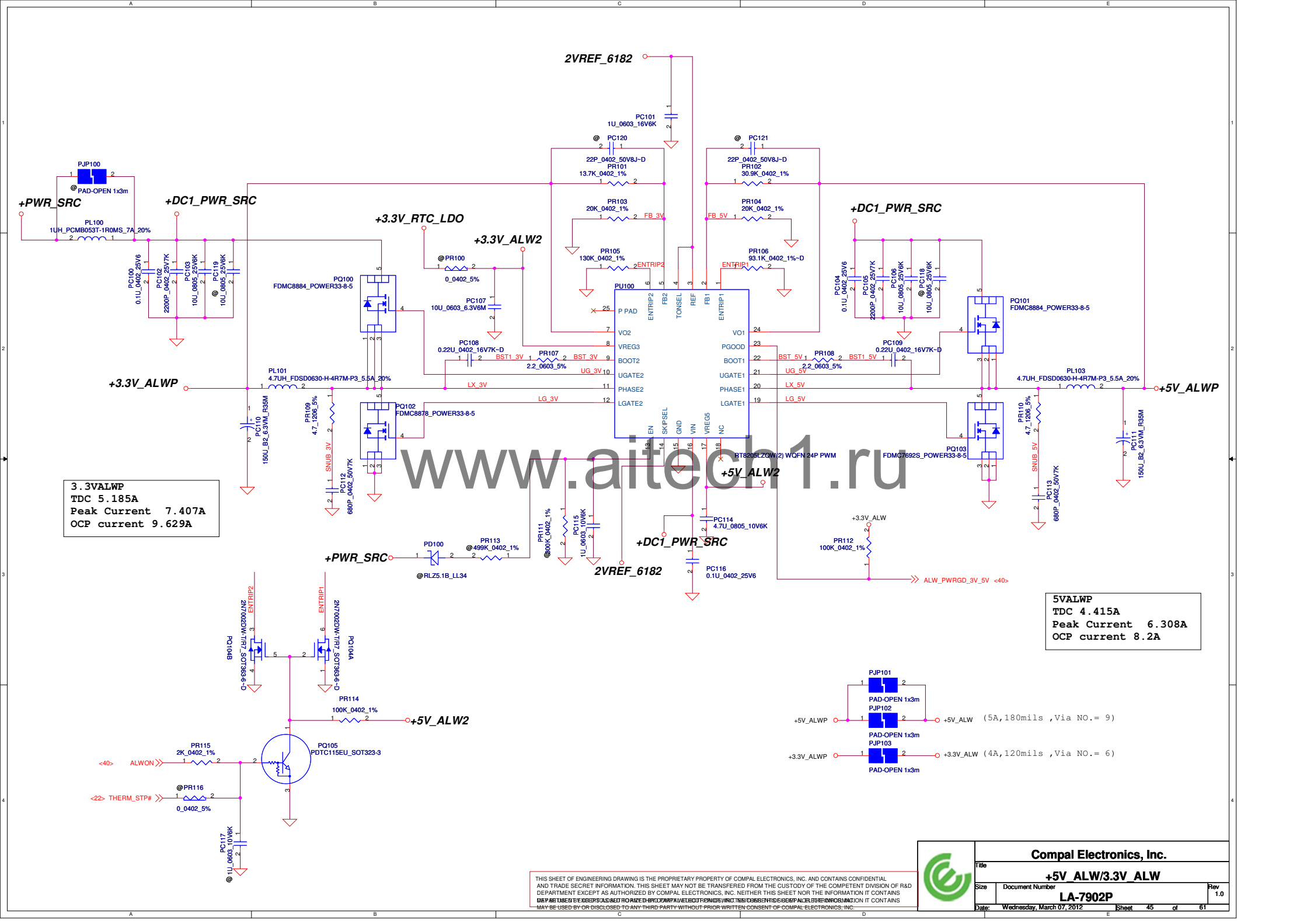
LED Circuit Control Table		
	SYS_LED_MASK#	LID_CL#
Mask All LEDs (Sniffer Function)	0	X
Mask Base MB LEDs (Lid Closed)	1	0
Do not Mask LEDs (Lid Opened)	1	1



Place SW2 between D23 and JPWR1 Top side for debug

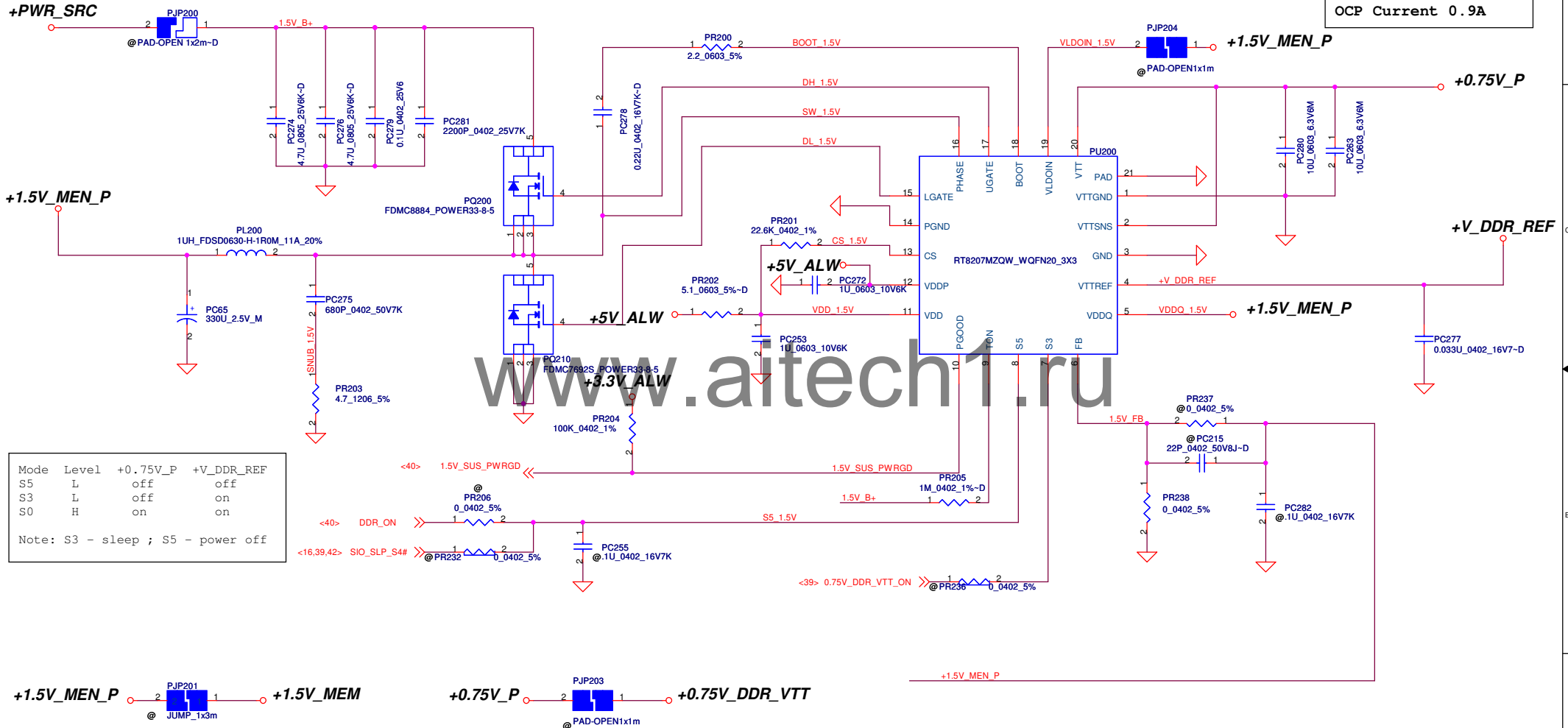
Place SW1 between D23 and JPWR1 BOT side for debug

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1.5Volt +/- 5%
TDC 9.74A
Peak Current 13.915A
OCP current 16.698A

0.75Volt +/- 5%
TDC 0.525A
Peak Current 0.75A
OCP Current 0.9A



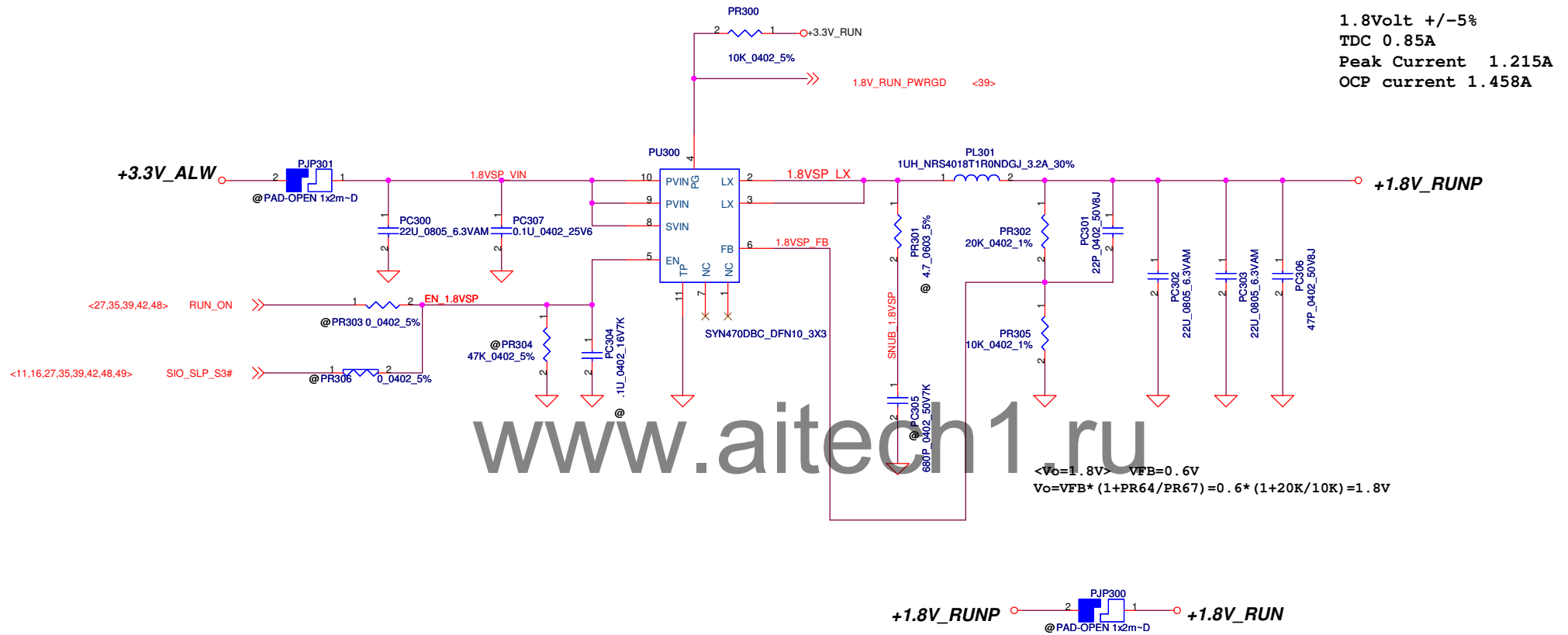
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Compal Electronics, Inc.

Title		
+1.5V_MEN/+0.75V_DDR_VTT		
Size	Document Number	Rev
	LA-7902P	1.0
Date:	Wednesday, March 07, 2012	Sheet 46 of 61

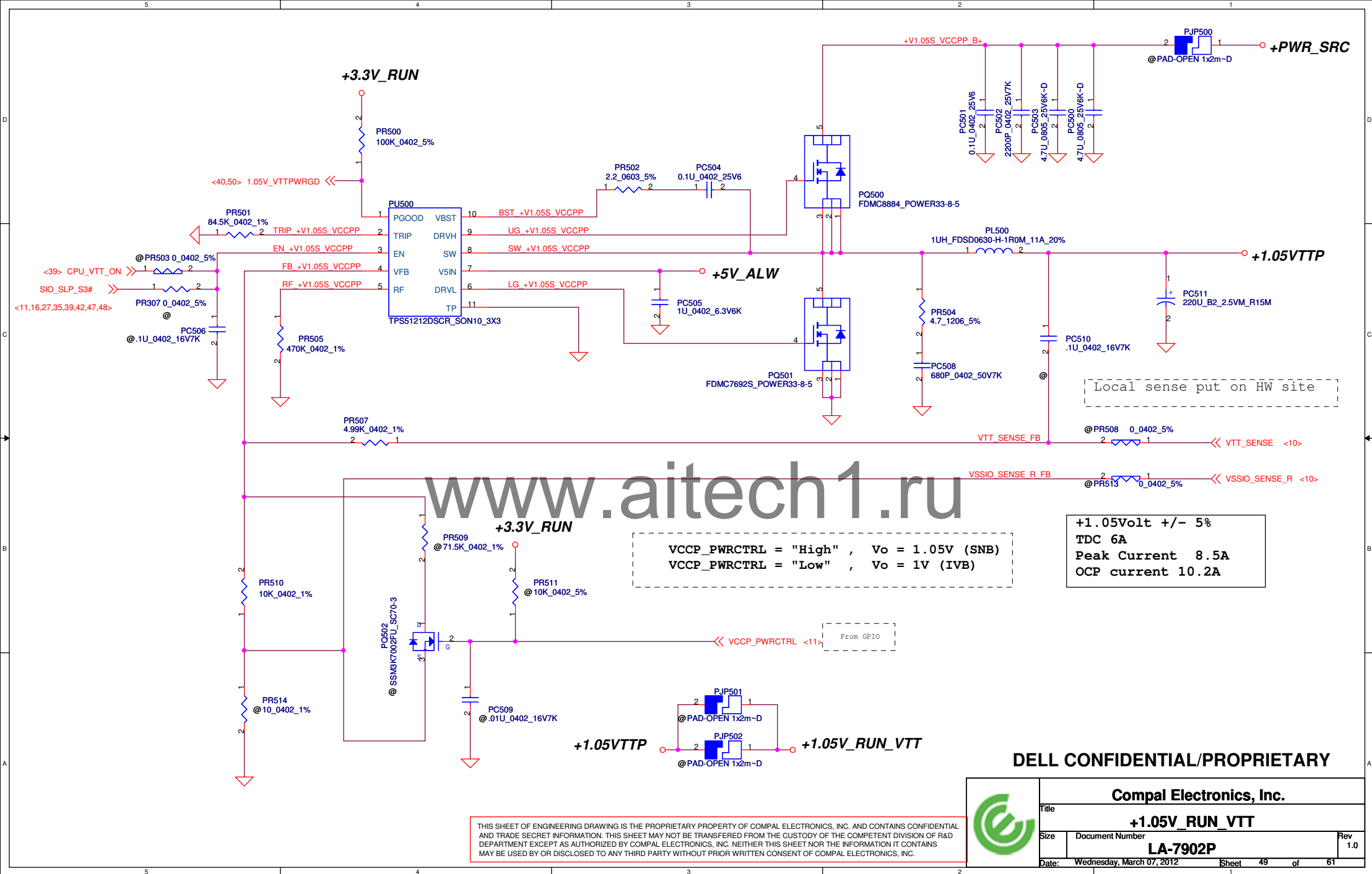
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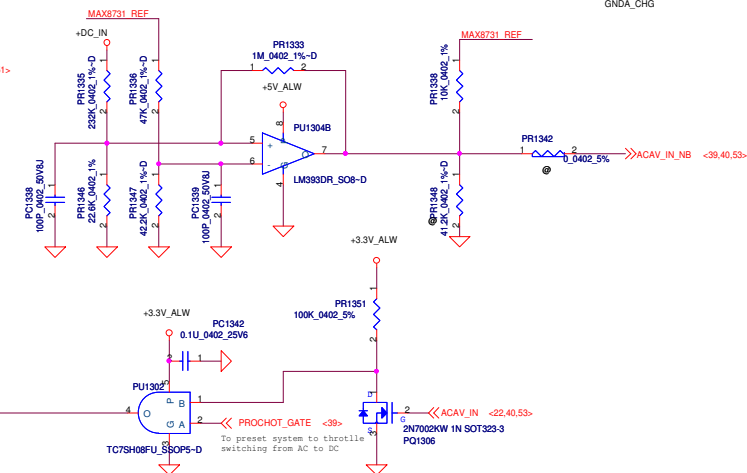
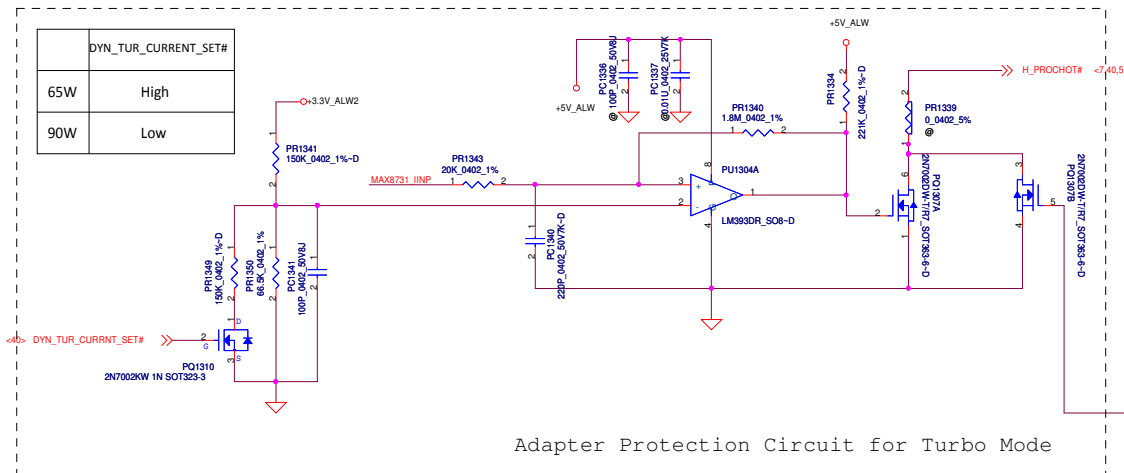
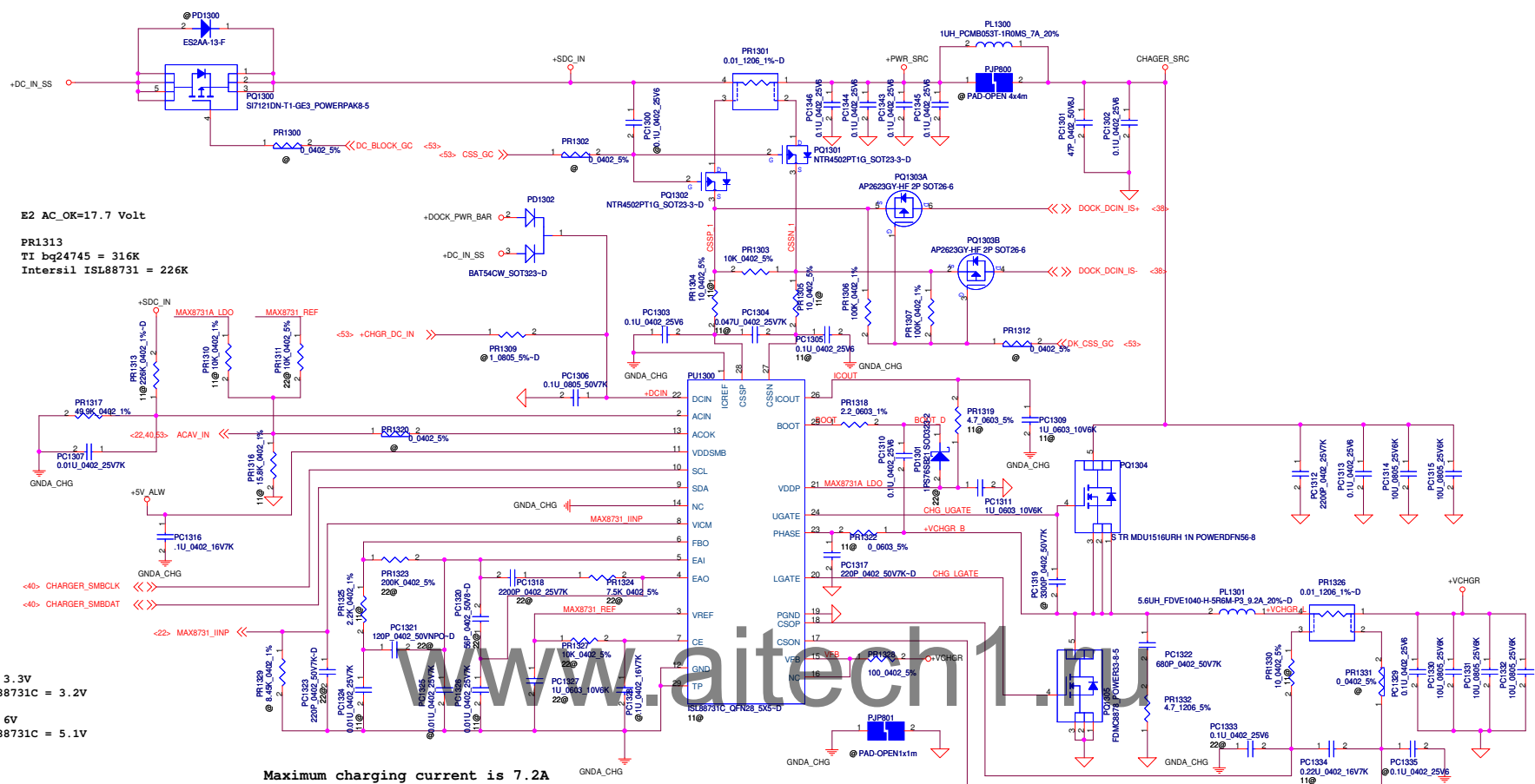
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				Compal Electronics, Inc.	
				Title	
				+1.8V_RUN	
Size	Document Number			Rev	
	LA-7902P			1.0	
Date:	Wednesday, March 07, 2012			Sheet	47 of 61



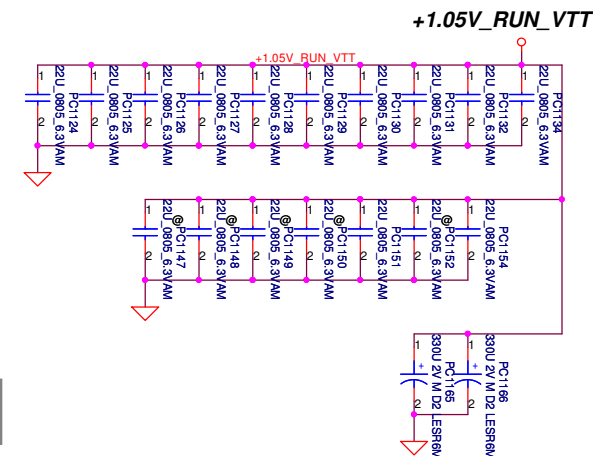
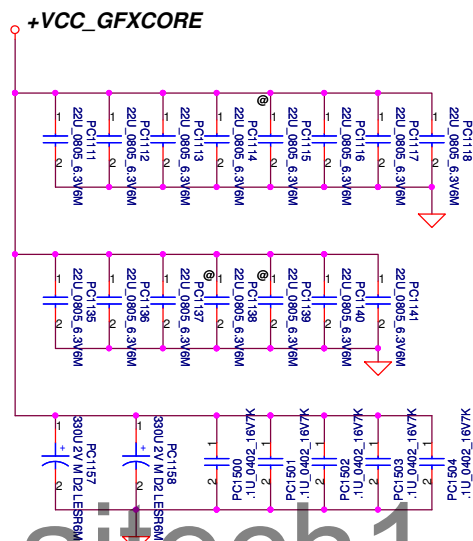
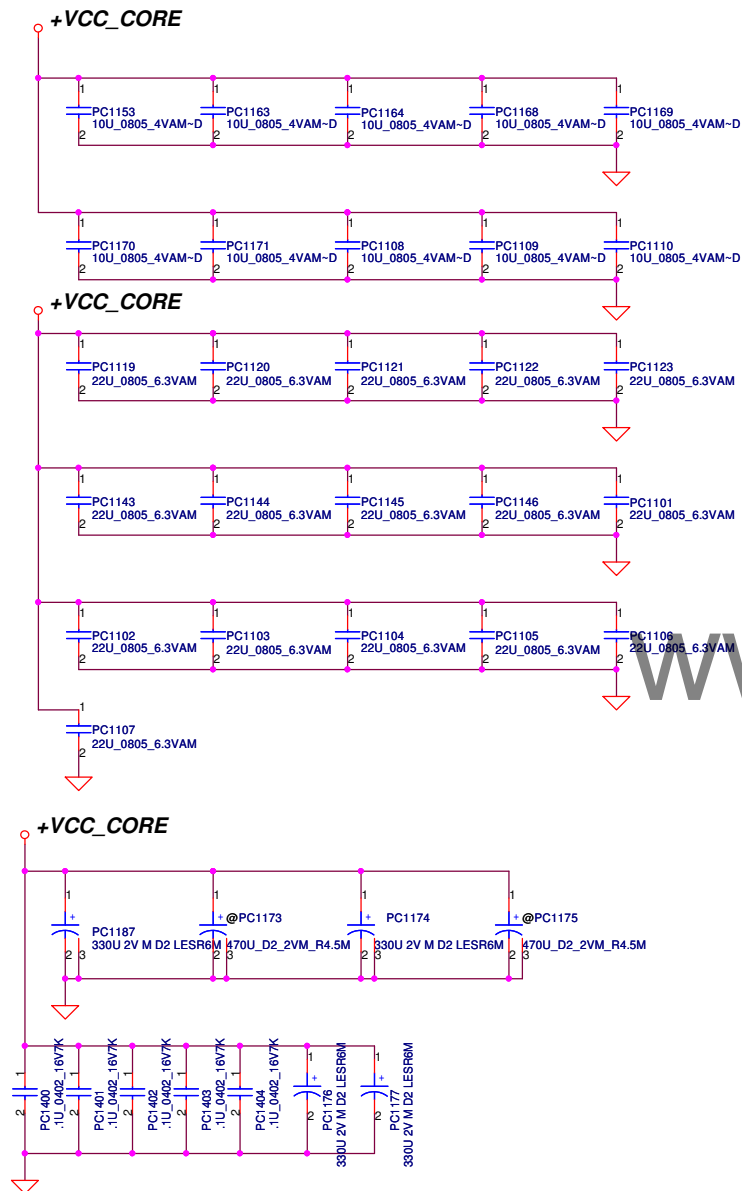
ISL88731C 11@
BQ24747 22@



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Compal Electronics, Inc.	
Charger	
LA-7902P	Rev 1.0
Date: Wednesday, March 07, 2012	Sheet 52 of 61

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	44	Power	8/18	Compal	ME design change.	PJPDC1 change from 7pin to 5pin	X01
2	45	Power	8/18	Compal	Main and 2nd IC common setting.	De-pop PD100, PR113, PR111	X01
3	45 46	Power	8/18	Compal	Prevent Jitter issue.	Add PC120, PC121, PC215 parallel with PR101, PR102, PR207	X01
4	51	Power	8/18	Compal	Prevent output voltage glitch when power up.	PU700 VCCP and VDD change form +5V_RUN to +5V_ALW	X01
5	53	Power	8/18	Dell	Change net name PBATT to SLICE_BAT_ON.	Change net name same as E4.	X01
6	50	Power	8/18	Compal	Reserve 0 ohm resistance for test.	Add PR90, PR91	X01
7	54	Power	8/31	Compal	Reserve cap for improve transient response.	Reserve PC1176	X01
8	54	Power	8/31	Compal	Change to green P/N.	Change PQ4, PC1153, PC1163, PC1164, PC1168, PC1169, PC1170, PC1171, PC1108, PC1109, PC1110, PC1187, PC1173, PC1174, PC1175, PC1157, PC1158 PQ1310, PQ1306, PC719 to green P/N	X01
9	48	Power	9/1	Dell	For support TL+TM	Change 6@ to pop for PC400~PC406, PC408, PL400, PQ400, PQ405, PR400~PR407, PU400. 5@ to @ for PR408.	X01
10	49	Power	9/1	Compal	For fix 1.05V_RUN_VTT on 1.05V	Depop PR509, PR511, PQ502. Change PR507 to 4.99k.	X01
11	45 52	Power	8/30	Compal	For reduce EMI radiation.	Pop PL100, PL1300	X01
12	51	Power	9/5	Compal	For reduce EMI radiation.	Change PL700 to SM01000DJ00	X01
13	45 46	Power	9/6	Compal	Change to green P/N.	Change PC107, PC263, PC280, PC405, PC505 to HF P/N.	X01
14	52	Power	9/13	Compal	For reduce EMI radiation.	Pop PC1400~1404, PC1500~PC1504.	X01

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Compal Electronics, Inc.			
Title			
PWR_PIR 1			
Size	Document Number		Rev
	LA-7902P		1.0
Date: Wednesday, March 07, 2012		Sheet	55 of 61

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Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
15	51	Power	9/14	Compal	For Vcore OCP setting	Change PC740 to 10nF, PR750 to 365 ohm.	X01
16	51	Power	9/14	Compal	For AXG OCP setting	Change PR702 to 2.61kohm, PR711 to 365ohm.	X01
17	51	Power	9/14	Compal	For Vender proposal	Change PC704 to 390pF, PC705 to 68pF, PC720 to 22pF, PC722 to 390pF, PC723 to 33pF, PR741 to 130kohm, PR703 to 130kohm, PC744 to 3300pF, PR754 to 649ohm.	X01
18	52	Power	11/17	Compal	Shortage issue	Change PQ1303 from NTGD416 to AP2623	X02
19	52	Power	11/17	Compal	Need ESD protected	Change PQ1306, PQ1310 from SB57002040L to SB000009Q80	X02
20	53	Power	11/17	Compal	IC version upgrade	Change PU11 from CD3301 to CD3301A	X02
21	45	Power	11/17	Compal	Shortage issue	Change PC110, PC111 from SGA00004E00 to SGA00002N80	X02
22	44	Power	11/21	Compal	HW requirement for S3 power consumption	PWR_SRC_S control signal change from +3.3V_ALW to PCH_ALW_ON	X02
23	52 53	Power	12/04	Compal	EMI requirement	add PC1343 PC1344 PC1345 PC1346 PC1347 (0.1U_0402_25V6)	X02
24	52	Power	12/04	Compal	EMI requirement	add PC1302 (0.1U_0402_25V6) PC1317 (220P_0402_50V7K~D)	X02
25	52	Power	12/04	Compal	Prevent COS.	change PD1301 from SCS00003M0L to SCS0000400L PD8 from SCS00004L0L to SCS00005C00	X02
26	54	Power	12/13	Compal	Prevent COS.	change PC1174 PC1176 PC1177 PC1187 PC1158 PC1157 PC1165 PC1166 to SGA00002U1L	X02
27	50	Power	12/13	Compal	Improve efficiency	change PR86 to 22K_0402_5%	X02
28	47	Power	12/16	Compal	Prevent COS.	change PL301 to SH00000MW00	X02


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Title PWR_PIR 2			
Size	Document Number LA-7902P		Rev 1.0
Date: Wednesday, March 07, 2012	Sheet 56	of 61	

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	11	HW	08/15/2011	COMPAL	INTEL review feedback	Add CC178,CC179,CC149,CC150	X01
2	32	HW	08/15/2011	COMPAL	Finger Print connector changed	Change JBI01 to pitch 1.0mm	X01
3	37	HW	08/15/2011	COMPAL	Sniffer Switch location changed	Change JSF1 to SF1	X01
4	14, 39	HW	08/15/2011	COMPAL	SMSC request to delete LPC_LDRQ0#	Leave LDRQ0# no connection on both of 5048 and PCH side Removed R743	X01
5	22	HW	08/15/2011	COMPAL	Removed reserve circuit for EMC4022	Removed R405,C280,R392,R394	X01
6	42	HW	08/15/2011	COMPAL	Load SW sources output rising time mismatch and COS. cost concern	Change back to E3 +3.3V/5V_RUN discrete solution Removed U78 and add Q55,Q61 circuit	X01
7	1, 29	HW	08/15/2011	COMPAL	Codec is change to 92HD93	Pop R162~R166 and de-pop U73,R1540 add R1641 connect the codec pin48 to U73 pin1	X01
8	29	HW	08/15/2011	COMPAL	Reserve co-lay with ALC290	Pop option for 92HD93/ALC290=>R1646/C1164; R1644/R1643; C965/R1642; Q107/R171 Reserve for ALC290 only: C1204, C1205, R1647, C1165, R1648 Reserve for 92HD93 only: R1645, C963 Add R174 depop and R175 pop	X01
9	20, 42	HW	08/15/2011	COMPAL	Vgs less than cut-in voltage in battery mode	Add control circuit QH6,R279,CH107 for +5V_ALW_PCH	X01
10	27, 28	HW	08/15/2011	COMPAL	Vgs of 5V MOS maybe large than max rating	Add R517, R518	X01
11	11	HW	08/15/2011	COMPAL	Follow INTEL PDDG 0.8	De-pop RC140	X01
12	40	HW	08/15/2011	COMPAL	Change board ID to X01	Change R875 to 130Kohms	X01
13	34	HW	08/15/2011	COMPAL	PCH GPIO52 need 8.2~10K pull up +3.3VS	Change R695 from 100K to 10Kohms	X01
14	23	HW	08/15/2011	COMPAL	CRT SW 2nd source TI, TS3V713 pin29 is VDD	Connect U18 pin29 to +3.3V_RUN	X01
15	16	HW	08/15/2011	COMPAL	+1.05V_M turn off before APWROK de-assert	Add UH5,CH108 6@ circuit reserve for VPRO	X01
16	41	HW	08/15/2011	COMPAL	Reset IC threshold voltage issue	Change U4 to RT9801A (threshold adjustable) Add R1649~R1654;Reserve R1655 and pop R1623	X01
17	26	HW	08/15/2011	COMPAL	DPX_CA_DET voltage too low through dongle	Change U21 and U24 to SA000005G0L	X01
18	17, 18	HW	08/15/2011	COMPAL	Request from INTEL review feedback	Pop RH332 for PCH_GPIO3 and RH180 for GPIO27	X01
19	42	HW	08/15/2011	COMPAL	Material changed	Power team request Q59 change to SB00000L80L	X01
20	43	HW	08/15/2011	COMPAL	White light LED brightness is abnormal	Change R934, R938, R939, R949, R958, R957 and R959 to 2.2 Kohms	X01
21	40	HW	08/15/2011	COMPAL	Reserve C1208 for ESD backup plan	Reserve C1208 for ESD backup plan	X01
22	11	HW	08/15/2011	COMPAL	S3 can't resume issue	Control 1.5V_VDDQ by EC. Pop RC79 and de-pop RC82	X01
23	15	HW	08/15/2011	COMPAL	Fine tune CLK EA	Changed RH311,RH314 to 10 ohm	X01

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HW_PIR 1			
Size	Document Number		Rev
	LA-7902P		1.0
Date:	Wednesday, March 07, 2012		Sheet 57 of 61

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
24	17, 18	HW	08/16/2011	COMPAL	INTEL review feedback	Change RH331,RH272 to 10K ohm	X01
25	34	HW	08/16/2011	COMPAL	WWAN card request	JMINI1 pin 1 connect to PCIE_WAKE#	X01
26	1, 14	HW	08/16/2011	COMPAL	ROM size changed	Change U52 to 8M and R936,R895,R897,R900 to 6@	X01
27	11	HW	08/17/2011	COMPAL	Material package changed	Change CC161~CC166 from 0402 to 0603	X01
28	42	HW	08/17/2011	COMPAL	BOM changed	Change Q60 to 6@	X01
29	31	HW	08/17/2011	COMPAL	Correct Lan power net name	Change LL1,LL4,LL6~LL8 pin 2 net from +3.3V* to +1.2V*	X01
30	39	HW	08/19/2011	COMPAL	GPIO signal name changed same as E/P	Change PBATT_OFF to SLICE_BAT_ON	X01
31	34	HW	08/19/2011	COMPAL	Material package changed	Changed C615,C1176	X01
32	37	HW	08/26/2011	COMPAL	Change audio connector pin definition.	Change JAUD1 pin15 to NC	X01
33	29	HW	08/29/2011	COMPAL	Remove reserve co-lay with ALC290 circuit	Remove below circuit: Pop option for 92HD93/ALC290=>R1646/C1164; R1644/R1643; C965/R1642; Q107/R171 Reserve for ALC290 only: C1204, C1205, R1647, C1165, R1648 Reserve for 92HD93 only: R1645, C963 Add R174 depop and R175 pop	X01
34	43	HW	08/29/2011	COMPAL	White light LED brightness is abnormal	Change R934, R938, R939, R949, R958, R957 and R959 to 1.2 Kohms	X01
35	25	HW	08/30/2011	COMPAL	Due to EMI HDMI test Fail, add EMI solution	Change resistor to Inductor Change R451, R459, R462, R466, R468, R469, R470, R471 to (Inductor CES symbol is not ready) Add C1209, C1210, C1211, C1212, C1213, C1214, C1215 and C1216 between Inductor and HDMI connector	X01
36	37	HW	08/30/2011	COMPAL	Follow CONN List_0824 Change JAUD1 to ACES_51522-02001-001	Change JAUD1 to ACES_51522-02001-001 and swap pin because pin1 definition different	X01
37	24	HW	08/31/2011	COMPAL	Follow CONN List_0824 Change JLVD51 to STARC_111H40-100000-G4-R	Change JLVD51 to STARC_111H40-100000-G4-R	X01
38	25	HW	08/31/2011	COMPAL	For EMI solution de-pop L19~L22 and pop 0ohm resistors(need change to Inductor)	de-pop L19~L22 and pop R451, R459, R462, R466, R468, R469, R470, R471(need change to Inductor)	X01
39	30	HW	08/31/2011	COMPAL	Change RL23 to 1.2k for IEEE EA	Change RL23 to 1.2k for IEEE EA	X01
40	30	HW	08/31/2011	COMPAL	Change 3.3V_LAN control signal pop option for TM/TL	Change RL46 to pop & RL47 to @	X01
41	14, 16, 19, 22, 30, 40, 42	HW	09/02/2011	COMPAL	Change pop option for TM/TL	Change 6@ to pop; 5@ to @ Change R871 pop, R877 depop	X01
42	37	HW	09/05/2011	COMPAL	Reserved 2 GNDA pins for Audio performance issue.	Add JAG1 2 pin connector.	X01
43	14, 15	HW	09/06/2011	COMPAL	Change Cap value for Crystal EA	Change CH2, CH3 from 15pF to 18pF Change CH18, CH19 from 12pF to 10pF	X01
44	25	HW	09/06/2011	COMPAL	For EMI solution change resistor to Inductor	Change 0ohm (R451, R459, R462, R466, R468, R469, R470, R471) to 9nH (L99,L100,L101,L102,L103,L104, L105,L106)	X01

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Compal Electronics, Inc.			
Title HW_PIR 2			
Size	Document Number LA-7902P		Rev 1.0
Date:	Wednesday, March 07, 2012		Sheet 58 of 61

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
45	15, 30, 40	HW	09/06/2011	COMPAL	Reserved TM LAN SMBus	Add QH8, RL48~RL51	X01
46	37	HW	09/07/2011	COMPAL	Change USB common choke from Audio/B to M/B	Add L107 & R1656,R1657	X01
47	07	HW	09/07/2011	COMPAL	PCH_PLTRST#_R & VCCPWRGOOD_0_R add 2 CAP to GND for ESD.	Add CC141 & CC142	X01
48	24	HW	09/08/2011	COMPAL	DMIC0 & DMIC_CLK0 add 100pF CAP close to JLVDS	Reserve C1217 & C1218	X01
49	36	HW	09/08/2011	COMPAL	Change USB3.0 CAP to 0.1uF	Change C412,C413,C414,C415 to 0.1uF	X01
50	29	HW	09/08/2011	COMPAL	Change AGND to DGND CAP to pop	Change C982,C985,C986,C987 to pop	X01
51	07	HW	09/09/2011	COMPAL	Change RC25 value for ESD	Change RC25 from 0ohm to 1kohm	X01
52	32	HW	09/09/2011	COMPAL	Swap JBI01 pin define	Swap JBI01 pin define	X01
53	29	HW	09/13/2011	COMPAL	IDT suggest exchange location R169~R172 & C973~C976. Change L91~L94 part number to 0ohm	IDT suggest exchange location R169~R172 & C973~C976. Change L91~L94 part number to 0ohm	X01
54	42	HW	09/13/2011	COMPAL	Change Q55,Q61 part for open soldering issue.	Change Q55,Q61 from DMN3030LSS-13 to AO4478L	X01
55	15	HW	09/13/2011	COMPAL	CLK_SMART_48M reserve 10pF CAP to GND for RF	Reserve CE18 to GND	X01
56	15, 30	HW	09/13/2011	COMPAL	Change LAN SMBus pop option.	Change QH8,RL50,RL51 to pop & RL48,RL49 to de-pop	X01
57	29	HW	09/15/2011	COMPAL	Follow EMI recommend	Change L91~L94 to 2A bead	X01
58	19	HW	09/15/2011	COMPAL	Change LH1 from bead to Inductor for CRT	Change LH1 to 1uH Inductor (SHI00007W0L)	X01
59	40	HW	11/28/2011	COMPAL	Crystal EA result, change CAP value.	Change C741,C743 to 33pF (PT Memo)	X02
60	39	HW	11/28/2011	COMPAL	EXPRESS card insert in 15" vPro can't power on issue	Change R760 to 20k ohm (PT Memo)	X02
61	40	HW	11/28/2011	COMPAL	Change board ID to X02	Change R875 to 62Kohms	X02
62	42	HW	11/28/2011	COMPAL	Rated Vgs of Q61 is 25V	De-pop R1627 (PT Memo)	X02
63	39	HW	11/28/2011	COMPAL	SMSC change 5048 pin A23 to GPIOIO	Re-link ECE 5048 symbol	X02
64	40	HW	11/28/2011	COMPAL	SMSC review feedback	Reserve R1658 and R1659 100Kohms to GND for I2S disabled	X02
65	41	HW	11/28/2011	COMPAL	Change reset IC to RT9818A-44GU3	Update U4 symbol and add R1629 for backup of inrush prevention. Change RSMRST# pull up with 100Koms. Pop R1655 and de-pop R1623.Delete R1649~R1654	X02
66	39	HW	11/28/2011	COMPAL	When suspend/resume cycles, wireless SW GPIO IRQs keeps giving	Add R771 pulling up to +3.3V_ALW for WIRELESS_ON#/OFF and de-pop R766	X02
67	27	HW	11/28/2011	COMPAL	Depop HDD control power circuit for cost down.	Depop R1624,Q28,R500,R499,R517,C393	X02

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Compal Electronics, Inc.			
HW_PIR 3			
Size	Document Number		Rev
	LA-7902P		1.0
Date:	Wednesday, March 07, 2012		Sheet 59 of 61

Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue	Solution	Rev.
					Description	Description	
68	30	HW	11/28/2011	COMPAL	Crystal EA result	Change YL1 to 3G025000FA1H, CL5,CL6 to 12pF. (PT Memo)	X02
69	11, 42	HW	11/28/2011	COMPAL	Change MOSFET to wihtout Schottky Diode	Change QC3 and Q59 to A04304L_S08	X02
70	34	HW	11/28/2011	COMPAL	S3 had leakage in +3/5V_RUN	De-pop R725, remove R695 and add +3.3V_RUN pull high at PCH side(RH361)	X02
71	32	HW	11/28/2011	COMPAL	TPM is changed to AT97SC3204-X2A18-AB	U39(TPM) is changed to SA00004WQ10(AT97SC3204-X2A18-AB) for WIN8 support	X02
72	32	HW	11/28/2011	COMPAL	+3.3V_RUN Giltch when AC plugin	Reserve D87, R1663 (pull high to +3.3V_RUN_TPM) and add R1662 for HW solution backup	X02
73	14~21	HW	11/28/2011	COMPAL	Change PCH to C1 version	Change UH4 to SA00005AG1L(HM77 for non vpro)	X02
74		HW	11/28/2011	COMPAL	Change RC value at Gate of MOS Load SW to modify power rail soft start timing	RC72 from 100K to 330K; RC143 form 330K to 1M; CC136 form 0.1u to 0.022u R412 from 100K to 470K; R1632 form 1M to 4.7M; C293 form 0.1u to 0.022u R507 from 100K to 470K; R517 form 1M to 4.7M; C400 form 0.1u to 0.022u R722 from 100K to 470K; R1625 form 1M to 4.7M; C644 form 4700p to 220p R729 from 100K to 470K; R1628 form 1M to 4.7M; C650 form 4700p to 220p R917 from 100K to 470K; R1617 form 1M to 4.7M; C770 form 4700p to 220p R920 from 100K to 470K; R1610 form 470K to 2.2M; C771 form 4700p to 470p R930 from 100K to 330K; R1611 form 470K to 1M; C773 form 2200p to 100p R906 from 100K to 470K; C763 form 2200p to 220p R912 from 100K to 470K; C766 form 470p to 220p	X02
75	36	HW	11/28/2011	COMPAL	Change P/N for HF	Change C412~C415 P/N to SE076104K8L	X02
76		HW	11/28/2011	COMPAL	For cost saving	Change 0 ohm to R-short	X02
77	37	HW	12/01/2011	COMPAL	Remove 2pin connector for Audio performance	Remove JAG1 2 pin connector.	X02
78	15	HW	12/01/2011	COMPAL	Add 10pF CAP to GND for RF request	SIO_14M add CE19(10pF) to GND	X02
79	35	HW	12/01/2011	COMPAL	Reserve 0.1uF CAP to GND for ESD request	PCH_PLTRST#_EC & EXPCLK_REQ# reserve 0.1uF CAP(CE14,CE20) to GND	X02
80	35	HW	12/02/2011	COMPAL	Reserve 0.1uF CAP to GND for ESD request	EXPRESS_DET# reserve 0.1uF CAP(CE22) to GND	X02
81	37	HW	12/05/2011	COMPAL	Follow CONN List_1130A Change JAUD1 to ACES_51522-0200N-P01	Change JAUD1 to ACES_51522-0200N-P01 and swap pin because pin1 definition different	X02
82	17, 38	HW	12/07/2011	COMPAL	EMI solution for E-Docking USB port	Swap USB Port7 and Port8 and reserve a choke(L108) at E-Docking side: Port7 from NA to E-docking ; Port8 from E-Docking to NA	X02
83	24, 32, 37	HW	12/07/2011	COMPAL	Change USB9,12,13 CMC to 180ohm for EMI request	Change L10,L52,L107 to SM070002X00(OCF2012181YZF)	X02
84	22	HW	12/09/2011	COMPAL	Thermal requests to change OTP from 88 to 92	Change R406 from 953ohm to 1.24Kohm	X02
85	41	HW	12/09/2011	COMPAL	To prevent inrush current at reset IC input	Change R1629 from 0ohms to 33ohm resistor	X02
86	25	HW	12/13/2011	COMPAL	Change HDMI R,C value for EMI request	Change R448,R449,R450,R452,R453,R454,R455,R456 from 680ohm to 604ohm; C1209~C1216 from 4.7pF to 3.9pF	X02
87	42	HW	12/15/2011	COMPAL	+3.3V_SUS sequence timing	R911 from 100K to 470K; R1618 from 1M to 4.7M; C767 from 4700p to 220p	X02
88	43	HW	12/15/2011	COMPAL	Change current limit resistors of LED	R934 from 1.2K to 910, R957 from 1.2K to 820, R951 from 330 to 200, R938 from 1.2K to 1.5K, R958 from 1.2K to 1K, R953 from 330 to 300 and R939 from 1.2K to 1.8K	X02

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Compal Electronics, Inc.



Title			
HW_PIR 4			
Size	Document Number		Rev
	LA-7902P		1.0
Date:	Wednesday, March 07, 2012	Sheet 60 of 61	

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Version Change List (P. I. R. List)

Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
89	38	HW	02/10/2012	COMPAL	Dalmore14 UMA hang on white screen issue when attached AC+media battery after hot dock.	Change R755 from 100k ohm to 10k ohm	A00
90	33	HW	02/10/2012	COMPAL	Change SD CLK damping resistor for EMI request	Change R676 from 33 ohm to 10 ohm	A00
91	43	HW	02/24/2012	COMPAL	Change current limit resistors of LED	Change R958 from 1K to 1.3K (ST Memo)	A00
92	40	HW	02/24/2012	COMPAL	SMSC creates a new catalog part number and IC marking for the MEC5055	Change U51 P/N to SA00003TZ2L	A00
93	40	HW	02/24/2012	COMPAL	Change board ID to A00	Change R875 to 33K ohm	A00
94	32	HW	02/24/2012	COMPAL	Change BOM option for TPM/TCM funtion	Change C550,C551,C552,C553,R659,R660,R1662,RH311 BOM option to 5@	A00
95	25	HW	03/02/2012	COMPAL	SMT request to change F2 footprint	For DFX concern of F2 2nd source, SP040003H0L, change F2 footprint to F_MF-MSMF050-2	A00
96	14~21,30	HW	03/02/2012	COMPAL	Change PCH chip P/N for X-build	UH4 is changed to SA00005AG3L(wait confirm with PJE)	A00
97	14	HW	03/02/2012	COMPAL	De-pop resistor on PCH JTAG for power saving	De-pop RH288, RH47, RH48 and RH49	A00

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Title

HW_PIR 5

Size **Document Number** **Rev**

LA-7902P **1.0**

Date: Wednesday, March 07, 2012 **Sheet** 61 **of** 61

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